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Editor's Page

Mid-Level Languages Being Developed

When the first hobbyists built their microcomputers they had to code their programs directly in machine language, but they soon wrote assemblers which enabled them to use easier to remember mnemonics for their source code. Language development continued and BASIC became the familiar language for the new micro owner. Today there is an almost overwhelming assortment of languages to choose from with something for every purpose, and it seems that there should be no reason to work on language development — but there are those who are not satisfied with what exists, and who feel the need to build on the foundation which has been established. Telling a software hacker not to waste his time on language development because everything has already been done is like telling an author not to write another novel because everything has already been written about, or telling an artist not to bother painting another picture because every kind of picture has already been painted!

While it IS true that there is a suitable language for almost every purpose, it is also true that there are some applications for which there is not an optimum language. As I have stated before, I contend that we should all be comfortable working in at least two or three languages and familiar with two or three more so that we can choose the best language for each application. There isn’t (and never will be) a perfect language which is optimum for every use! When we learn our first language we think that we can do everything with it, and although it is cumbersome for some uses we plod along because we don’t want to give up our hard won skills and start over again with a new language. But, learning the second language is a lot easier than the first one, and it enables us to see how clunky the first one was in some areas (and also how we miss some of its features in the new one).

If you haven’t already read Kibler’s Computer Corner turn to the back page and read it now, because Bill’s thoughts about languages have been voiced by others several times during the past two months.

Assembly is a great programming language and the small ROMable programs run fast — but coding can be tedious for large programs, and I/O, screen-handling, menus and floating point math routines can be a real bitch to code. Developing a non-trivial assembly language program is awkward because you have to write the source code with an editor (I use WordStar), assemble to a HEX file, and load that to a .COM file for a trial run. Then back to the editor for any revisions and through the whole routine again. An assembly language program is also very difficult to understand and update in the future, even if you wrote it yourself, and can be almost impossible to decipher if written by someone else.

“There isn’t, and never will be, a perfect language which is optimum for every use.”

A program properly written in a structured high level language such as C or Turbo Pascal takes less time to write and is much easier to maintain or modify if your requirements change, but some sections may be too slow for time-critical real world control applications or computer bound portions. Some language implementations also have a large run-time code which results in large files for even small programs. I really like assembly language for the high speed routines and for initiating system port drivers, but I don’t like the time and effort involved in non-critical areas such as screen messages and menus.

The experts say that you should fully define the objectives and design your program before writing any code instead of hacking away at it and making

(Continued on page 44)
Extending Turbo Pascal
Use Procedures and Functions to Develop Your Own Version
by Jerry Houston

Because Turbo Pascal’s procedures and functions allow the use of local variables that don’t conflict with other parts of the program, and because parameters can be passed to them, it is really easy to define new keywords, in effect. That means that a version of Turbo Pascal can be developed that meets the individual and personal needs of a particular programmer, whether he or she is a writer of business report programs or of economics simulations or engineering number-crunchers.

For example, when printing business forms like an invoice, I wanted a simple command that would establish a print position on the printer, such as the standard Turbo Pascal procedure GOTOXY(h,v) which places the cursor at a specified horizontal/vertical position on the CRT screen. A simple procedure can be written to do this, effectively adding another command to the language. Anyone can extend the language with keywords that are familiar from another language, if they want. Thus, a Turbo Pascal instruction could be written as such:

```
HTAB(20); VTAB(10); WRITE(‘Hi, there!’);
```

which would do exactly what an old Applesoft programmer would expect it to, except in this case, with the printer.

So that the runtime library isn’t overly large, little-used commands are not built into Turbo Pascal, but left up to the user. Exponentiation isn’t accommodated directly by any version of Pascal C that I know of, but can be added easily, using the LOG function, which is available. Thus, the MBASIC statement:

```
100 C = A A B
```

could be implemented in Turbo Pascal such as:

```
C := POWER(A,B)
```

once the function has been written and tested. Significantly, it never has to be written or tested again, and can be used whenever needed.

Because of the block-read and block-write features of the Turbo editor (it works just like WordStar® CTRL-K R and CTRL-K W), it’s easy to save procedures and functions separately on a disk for future use in other programs. The {$I+} (include) compiler directive will even merge a procedure with a source as it is being compiled, making it unnecessary even to read it in with the editor. A whole personal library of often-needed procedures and functions can be read into the source code with just a few keystrokes, and seldom-used ones can be added individually at compile-time.

The proper construction of procedures and functions, and the proper use of local variables with them, is an important topic for anyone who’s starting out with Turbo Pascal, because it helps them to exploit the full power and versatility of this beautiful language. Procedures and functions in Pascal share many qualities—for convenience, I’ll refer to both of them as “procedures” from now on, until the time comes to make a distinction.

**Pascal vs. Basic**

For those who are just now being introduced to Pascal (due to the amazing popularity of Turbo), I should pause to explain two of the major differences between Pascal and BASIC, a language familiar to nearly everyone. The first of these differences lies in how variables are used, the second in how programs themselves are structured (or not).

Disorganized program design is easy for anyone, using languages that do not lend themselves to good structure. BASIC is a very unfortunate choice for a beginner for this reason, but it’s the one to which most of us were first introduced. Not only does BASIC — when used without the benefit of training in structured design — encourage pointless jumping from one place to another within the program, but it allows new variable names to be “invented” at any time. Misspell a variable somewhere in that maze of instructions, and BASIC will go along with it without question, even though it means disaster for the program. BASIC has single-handedly brought a whole new meaning to the term “debugging.”

Pascal, on the other hand, requires that variables be defined before they’re used, and that they be declared according to their intended use. There are many reasons for this requirement — all of them excellent. For example, if a string variable is used in BASIC, it’s stored in memory the first time it is given a value. The next time a value is assigned to the same string variable, it can’t be stored at the same place, as its length as well as its contents might change. So a different chunk of memory is used, and a table is updated to point to the new contents of that string variable.

In a program that sorts a large list of string variables, it’s likely that a particular variable like COMPARE will get thousands of different values stored in it in the sort is finished. Each time this happens, more memory is used, until BASIC finally runs out of variable storage space and has to stop to GARBAGE COLLECT, the usual term for reclaiming the space used by all the previous contents of a string. This process can actually stop the execution of a program for minutes at a time, depending on the computer.

Pascal, on the other hand, assigns ONE AND ONLY ONE area of memory to store the contents of any variable, including a string. It does this by requiring that the user declare not only the NAME of a string, but also its maximum LENGTH. Since the same storage location is re-used each time a new value is assigned, there’s no such thing as GARBAGE COLLECTION in Pascal.

Since all variables are declared before they’re used, it is a simple matter for the Pascal compiler to call errors to the programmer’s attention. If a variable name is misspelled part way
through a program, the compiler will, in effect, say "What's this?" when it sees the error.

Turbo Pascal sets aside the amounts of memory shown in Figure 1 for the particular types of variables, and that same space is ALWAYS used to contain the current contents of that variable.

Using the keyword ABSOLUTE, a variable can be assigned a memory location of the programmer's choice, or Turbo can be allowed to keep them in a data storage section.

Like variables, procedures and functions must be declared before they are used. When the compiler reads through the list of source code to convert it to machine language, each time it sees a reference to a procedure or a function, it must already know what that operation requires. Thus, Pascal programs are said to be designed from the top down, and written from the bottom up. In the design stage, the overall job is split into tasks that need to be done. Each task is split further, into smaller tasks, until finally procedures can be written easily to accomplish those tasks. Writing the program is simple then — just write the code needed to execute each of the tasks, then write the MAIN LOGIC that asks for them to be done as needed.

Some excellent programmers take a different approach, preferring to design the program by writing the main logic itself first. Using this method, the main logic is written referring to procedures that haven't yet been coded, with no thought given to HOW each procedure will be written. Once the overall logic is complete, the programmer goes back and writes the procedures in detail.

Because it's so important to writing PORTABLE sections of code — ones that can be used in program after program — the concept of GLOBAL and LOCAL variables needs to be understood here.

GLOBAL variables are defined at the beginning of the entire program, and they exist to all parts of the program — the main logic and all the procedures. This is the familiar approach used in languages such as BASIC. LOCAL variables are defined at the beginning of each of the individual procedures instead, and have meaning only to that procedure and to other procedures that it calls. This concept is called SCOPE.

That means that I can define a variable called COUNT in several procedures, and use it as a counter in each of them, and never have to worry about whether one part of the program will change the value of a variable used in another part! Each version of COUNT will be stored separately, and will exist ONLY to the procedure in which it is declared, an example of local scope. If, however, I declare a variable called COUNT at the beginning of the program, then it's fair game for each procedure in the program. Any of them can change its value, and only ONE version of it exists in memory. It's easy to see that there are good uses for local variables, just as there are good reasons to use global variables, depending on the logic of the program.

Procedures

All implementations of Pascal, including Turbo Pascal, depend heavily upon procedures, which are sections of code that are written to accomplish a particular task. One of the greatest strengths of Pascal as a language is that it encourages the thoughtful design of a program through top-down structuring techniques, as mentioned above. Another strength is that procedures, once properly written, can be used again and again in programs that require the same simple task.

Since procedures are called when needed just by mentioning their name, the language itself can be extended in any way the programmer desires. Consider the case of GOTOXY(). It's a function that's provided with Pascal, one the user doesn't have to write, and is part of the Pascal Library (runtime package) that becomes a part of each program when it's compiled. GOTOXY() positions the cursor on the screen, so that a non-scrolling display can be written. The horizontal position is the first parameter in the parentheses following the procedure name (GOTOXY), and the vertical position is the second parameter.

Consider a program that needs to ask a large number of questions, and get a lot of answers from the operator. Instead of displaying questions one at a time and prompting individually for the answers, the programmer might prefer to display all the questions on the screen at one time, then move the cursor to the position following each question and ask for the answers, one at a time. To put the cursor in the tenth horizontal position of the fifth screen line, the procedure would be called by code such as:

GOTOXY(10,5): Read(Answer);

Consider now that an invoice is to be written, and you need the PRINT
HEAD, not the cursor, to go to a particular position on the paper before printing. The answer is to write a suitable procedure, then save it on a disk for future use whenever you need the same feature. The following example (which works with the Kaypro/II printer) can be modified easily for printers that use other control codes for print head movement:

Procedure PrintAt(X, Y: Byte);
Begin
  Writeln('.#27#99.chr(X);'
  Writeln('.#27#11.chr(Y);'
  End;

The first line identifies the code as a procedure, and names it PrintAt. The parameter list following the name shows that two parameters will be passed to this procedure, and locally calls them X and Y. Each of these parameters is identified as a Byte, so they can take on values from 0 to 255, quite appropriate for the purpose. The BEGIN and the END show the compiler what code is actually part of PrintAt.

The procedure PrintAt calls another built-in procedure, called WRITE. The first parameter for WRITE is "lst", which tells Pascal that this output is to be sent to the LST: device, the printer. (When a WRITE procedure is used to send data to the CRT screen — the most common case — the first parameter is unnecessary, as the default is CON:). The printer is sent the values which represent ESC CTRL-I for a horizontal tab, and the character which represents the position wanted, CHR(X). Then it's sent the values which represent ESC CTRL-K, for a vertical tab, and the character which represents the vertical position wanted, CHR(Y).

Suppose now that your invoice form needs the variables Name and Address printed at column 20 on lines 5 and 6, and the variables City, State, and Zip printed starting at column 20 on line 7, with a comma between City and State, and two spaces between State and Zip. The code in the main logic to accomplish that would simply be:

PrintAt(20,5); Writeln(Name);
PrintAt(20,6); Writeln(Address);
PrintAt(20,7); Writeln(City, ', ' State, ' Zip);

Like the built-in procedures Read, Write, and GotoXY, you now have a brand-new — and incidentally, very useful — procedure called PrintAt at your disposal. All that's needed to include it into each source code as you write your programs.

Such pieces of source code can be included automatically, thanks to the compiler directive {$I}, the INCLUDE directive for the Turbo Pascal compiler. To include the procedure PrintAt with another later program (assuming that PrintAt has been saved to disk), only the instruction:

{$I PrintAt}

is needed in the source, before PrintAt is referenced by name. The compiler will stop reading from the source code file that it's working with, and read in PrintAt from the disk, then continue with what it was doing.

Alternately, the Turbo editor can be used like WordStar to include such sections of code with the source. Place the cursor where you'd like the procedure to go, then type

CTRL-K R

The editor will respond by asking what file name to read from, and you'll type

PrintAt

An extension of .PAS is assumed when using the editor to read or write files, and will be added automatically unless you add a dot to indicate no extension, or add a dot and a different extension. Then your file name and extension will be taken literally.

Upon finding out the name of the file you want to include, the Turbo editor will read it in, and highlight the text if your terminal is capable of that. (To get rid of the highlighting, CTRL-K H works fine, again just like WordStar.)

This technique can be used to include a whole library of needed procedures, and the user is free to develop several libraries for special purposes. When additional procedures are stored together in a single file, as a library, less disk storage overhead is required, and the language can be extended considerably with only a few keystrokes.

It's probably obvious by now, but the reverse of this routine is useful to save a procedure to disk. If you've just written a terrific procedure that will simplify all your future programming, take the time to mark the top of the block...
with a CTRL-K B and the bottom with a CTRL-K W and give the editor a file name when it asks. Just like you know what.

Functions

All the manipulation techniques described so far apply equally to procedures and functions, but the two are somewhat different in use. A procedure is like a tiny program that accomplishes a single useful task, whereas a function is like a variable that takes on a particular value according to specified operations.

Since all the built-in procedures and functions of Turbo Pascal are included as part of the runtime library that becomes a part of each compiled program, routines that are not often used are not built-in for reasons of space. These built-in features take up nearly 1K of memory, meaning that the very smallest of programs, when compiled, will be at least 10K long. It's amazing what can be accomplished with that 10K of library, but that's not the point.

Everyone will sooner or later miss a favorite keyword from another language. It's inevitable, so let's expand on an example mentioned briefly before. BASIC offers easy exponentiation using the ^ operator. To find the cube of the variable A, for example, one can express it as A^3. Such features are not part of Pascal, because the code required to execute them would become part of every compiled program, including the thousands of programs that never have to perform such tasks.

It's at times like this that the extendability of Pascal really comes through. Here's a function called POWER that will provide the same feature in Pascal.

Function Power(X,Y:Integer):Integer;
Var R:Real;
Begin
R := Exp(X*Ln(Y));
Power := Round(R);
End;

The first line names the function, just as we named a procedure in the PrintAt example. In the parentheses are defined variables representing the two values that will be passed to this function (the parameters). Variables X and Y are to be integers, as is the function 'Power' itself.

The local variable R (for result) is assigned as a Real. R is arrived at using the EXP (exponent) and LN (natural logarithm) functions, which are built-in. Then R is rounded off and stored in the function Power. This function was written for the purpose of setting and clearing bits within a byte, so it was designed as an integer function. It could easily be rewritten as a real function for floating-point math.

Again, the BASIC code

\[ A = B \times C \]

can be duplicated in Pascal as

\[ A := \text{Power}(B,C) \]

and once more the language has been extended with the addition of a new keyword.

Portability

In this case, portability refers to the quality that lets a function or procedure be used with any program, not just the one for which it was originally written. To be truly portable, procedures and functions should be written using parameters when needed, and using local variables. These local variables should not be used as global variables in any of the programs. An easy way to accomplish that is to use single-character variable names for local variables, and variable names of more than one character for global variables. This isn't a rule of Turbo Pascal, but one I find useful for mysef f.

Note for those who are REALLY new to Turbo: All identifiers, such as variable names, procedure names, and function names, can be up to 127 characters long, with each character significant, and case is ignored. That means that a variable that refers to net profit before taxes can be called:

```
NetProfitBeforeTaxes
```

if you really want to, and a procedure that calculates a standard deviation can be called:

```
CalculateStandardDeviation
```

The compiler tokenizes all these names, so being descriptive doesn't cost anything at all, except maybe a little extra typing. Well written, a Pascal program can be darn near self-documenting, and a joy to modify at a later date, if that ever becomes necessary.

Needless to say, since Pascal programs are generally made up of many procedures, some of those will certainly NOT be of use in other programs. Using the concept of top-down design, a single large task is divided into two or more intermediate tasks, which are each further broken down into two or more smaller tasks.

```
PROGRAM

VAR

CONST

ReverseeOn = String[3] = 'R27' = 'R8'

ReverseeOff = String[3] = 'R27' = 'C8'

HalfInOn = String[3] = 'R27' = 'B1'

HalfInOff = String[3] = 'R27' = 'B3'

FlashOn = String[3] = 'R27' = 'B3'

FlashOff = String[3] = 'R27' = 'C3'

UnderlineOn = String[3] = 'R27' = 'B3'

UnderlineOff = String[3] = 'R27' = 'C3'

CursorOn = String[3] = 'R27' = 'B4'

CursorOff = String[3] = 'R27' = 'C4'

VideoOn = String[3] = 'R27' = 'B5'

VideoOff = String[3] = 'R27' = 'C5'

MessageCursor = String[3] = 'R27' = 'B6'

ReturnCursor = String[3] = 'R27' = 'C6'

StatusLineOn = String[3] = 'R27' = 'B7'

StatusLineOff = String[3] = 'R27' = 'C7'

DeleteLine = Char = 'M'

DeleteToEnd = Char = 'N'

Bell = Char = '8'

ClearScreen = Char = 'H'

Home = Char = '9'

SerialPort = 4

StatusPort = 6

Mask = 1

BaudPort = 9

BaudRate = 14

Listing 1
```
Soon these bottom-level tasks are small enough, and simple enough, so that they're easily coded into procedures. It is these bottom-level tasks that are usually worth saving for future use. It makes good sense to set aside a procedure like PrintAt, since it would almost certainly be useful in many kinds of programs. A procedure that gathers answers to a series of prompts used in one particular program, though, wouldn't be very useful elsewhere.

Examples

Some examples of procedures and functions that I've found useful in many programs are shown in Figure 2. In each case, I've identified the purpose, but I won't necessarily point out all the details of Turbo Pascal syntax, as this part of the article is intended for programmers who have and use Turbo Pascal.

It should be obvious that certain escape sequences, especially, are system-dependent, and that they will need to be modified a little (or maybe a lot) to work with a different terminal, printer, or other peripheral. Where this is not obvious, I'll point it out. In these particular examples, specific escape sequences are appropriate for the computer I use most often, a Kaypro 4-84 and the Kaypro (Juki 6100) letter-quality printer.

Those who happen to use the same equipment should be able to copy these routines directly. Those with other gear should not find it difficult to substitute small portions of code that will work for them. Many readers will find these examples immediately useful, but that's not the real reason for sharing them. These have been short, easy to understand procedures and functions, but the same principal can be applied to complex — but routine — procedures. The important concept is nearly every command you've ever wanted in a language can be developed as an extension to Turbo Pascal, making it one of the most powerful languages that was ever this easy to use.

I want to leave one last recommendation that will save a lot of time and trouble, too, although it doesn't have a thing to do with procedures or functions. It DOES provide another way to define more keywords.

Many programs can make use of the terminal capabilities of a computer, such as commands that clear the screen, home the cursor, print reverse images, highlight, or even turn off the video display when a key is not pressed for a long time. In some cases, these terminal attributes make use of some of the ASCII characters above the normal 0-127 range used for printing and ordinary control characters. In other cases, such as with the Kaypro, these are gained by printing an Escape
sequence that the terminal recognizes as a special attribute request.

As easy as reading a procedure from a disk file, a text file can be read that contains constant assignments for all these attributes, with names that are easily remembered and used in one program after another. This file is, actually, the beginning of a Pascal program, with all these constants assigned. If space is at a premium (hardly ever the case, with Turbo's virtual memory capability), any that are not used for a program can be deleted when the program is finished. It usually doesn't hurt anything to leave them, though, and they'll be available if you decide later to "spruce up" the output.

During the actual coding of the program, these attributes can be called upon by "Writing" them. If a particular action is required, like turning off the cursor during graphics routines — to make a cleaner display — it can be handled separately, as in:

Write(CursorOff);

If an attribute has something to do with a value that's going to be written anyway, it can simply precede it or follow it, like any other variable, such as:

Write(FlashOn, 'This message flashes..', FlashOff);

I use a file called STARTUP.PAS to begin nearly every program I write. A version of it is shown in Listing 1.

Again, this file will be of special interest to Kaypro '84-or-later owners, but owners of other equipment should appreciate the IDEA.

On work disks that I use for a special purpose, such as analog/digital data acquisition programs, I keep a special version of STARTUP.PAS that contains, in addition, functions and procedures for operating an ADC-1 Data Acquisition and Control System. On disks I use for business programs, I keep a copy of STARTUP.PAS that includes procedures and functions for getting dates and times from the clock card, printing datelines, and so on.

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Unsoldering: The Arcane Art
Second in a Series on Soldering, Unsoldering, and PC Board Repair
by James O’Connor

Why do you think they put erasers on the end of pencils? Who hasn’t heard that old chestnut, and the answer is because people make mistakes, mistakes, eras, errors, miscalculation. The spelling checker program will blow a binary fuse when it tries to digest that sentence.

Just as things go wrong when using a pencil, they also go awry when soldering. From the moment you begin to solder things together, inevitably you’ll solder the wrong thing in the right place, or the right thing in the wrong place. And if you get really involved in electronics you’ll want to unsolder components because they are under suspicion of malfunctioning.

Having determined that you must unsolder something you might naturally consult your reference material for some helpful hints, and if your reference library is similar to mine you may find a glaring lack of any help. When I recently reorganized all my literature I could find practically nothing on unsoldering among the dozens of features on soldering. Had I stumbled upon some dark and mysterious art known only to a select few? Some ancient ritual marked by obscure incantations, oaths, and secret hand clasps? Yes! At least the part about odd hand clasps, more about that later, and as to the mumbled oaths almost any will do if they benefit the process.

But seriously, the true purpose of this article is to delve into the intricacies and problems of unsoldering as fully as space will allow. In the initial article in this series on soldering it was stated that soldering is mostly science and partly art. By science is meant hard knowledge about the process and what makes it work, by art is meant the experienced judgement that comes from actual practice. Unsoldering on the other hand, hands that is (more than two are very useful, is more like 50/50 science and art. Or to phrase it another way, knowing what to do is equally as important as knowing when to do it and when not to.

We begin by reviewing the theory of soldering as it applies to unsoldering, then we ramble into the jungle of unsoldering tools including the techniques of using them. Next we’ll probe the art part, applying the right tools and techniques. Followed by the main skirmish, unsoldering a DIP IC which leads us to all the things that can go wrong trying to unsolder a DIP. We finish by discussing construction methods and how they affect the whole problem of unsoldering.

Theory

In the first article we began by talking about welding — separating things that have been welded together is very straightforward, since welding makes one object out of two the only way to reverse the process is to simply cut the one object into two pieces. We talked about welding to emphasize that soldering is different, we proposed the analogy that repairing is more like gluing things together, that solder can be thought of as a heat activated metallic ‘glue’. Glues stick things together so if the glue can be unstuck, the parts can be separated.

Glues come in several different types and the properties of each type affects how to unstick them. Some glues dry very brittle and can be separated by fracturing them, the old hide glue used on early furniture behaved this way, it would fracture with age which is why old furniture often has wobbly joints. Some glues never really harden, they just stay sticky, these can be defeated either by using a solvent to dissolve them or in some cases by heating the glue until it releases, the glue used on floor tiles is like this, heat it up and you can pry up the tile. Modern white glue that is used for wood and paper is made from chemicals that are very similar to the actual binders in natural wood, and these blend right into the surface structure. If they are waterproof then it may be virtually impossible to unstick things made with these glues, imagine the fix our modern world would be in if the glue used to make plywood was not as strong as it actually is.

Solder is very much like the modern glue that blends into the surface of a material, indeed it could be said that solder was the first example of this type of ‘glue’. Fortunately though solder can be affected by the very thing that makes it work in the first place, heat! Obviously then the first step in unsoldering will involve reheating the joint. It might seem to be the only thing required, just heat up the joint and separate the two parts. In some instances this will work, but most of the time there are some real difficulties with this alone. For instance since heat is involved you can’t use your fingers to reach in and pull things apart, not without suffering severe burns to your fingertips, a very painful burn, indeed. Some sort of forceps or pliers must be employed, which can be awkward. Because using tools is so tricky, you may be tempted to heat the joint longer than necessary to be sure that the solder is liquefied, that can lead to physical damage to adjacent parts, especially the circuit traces on the board. Obviously we won’t be discussing ways to repair damaged traces until the next article in this series you’ll want to avoid this problem for now.

Solder is interesting because it has properties similar to the three different glues mentioned above. If heating a joint and pulling things apart is not always practical then the next step would be to remove the solder. Since solder invades the surface of the metal it is not possible to remove it all but if only the surface coating remains then the resulting joint will be extremely weak and should fracture like the hide glue with gentle manipulation.

Clearly we can’t simply swab up molten solder with a Q-tip or even the ‘quicker picker upper’, a paper towel, hot solder would burn these materials and being organic they would not readily absorb a hot metal. We could use some sort ofwick to absorb the solder as long as the wick is compatible with solder and later we will talk about such products. Another approach would
be to vacuum it up with a wet/dry vacuum cleaner, which would indeed work but is actually a bit too elaborate. Instead there is a whole array of tools ranging from the super simple to utterly elaborate that use vacuum to suck up the liquefied solder.

**Tools and Techniques**

The first tool we'll discuss is the soldering iron used to reheat the joint. No mystery here, simply select the same size iron and style of tip as you would have used to make the connection (see the first article in this series for details). To use the iron, first heat it up and properly tin it, then apply it to the joint so that it touches the lead and sits on top of the solder fillet. This causes the heat to flow through the lead into the hole and on to any solder on the other side of the board. This is important when working on double sided boards, and works just as well on single sided ones. The solder fillet will tell you when it's molten because the tip will slip down into it. Keep the iron in place for one second more and the joint is all set for the next step. Occasionally, it won't be possible to position the iron as described, in that case apply the iron as best you can for no more than about one second longer than would be needed to make the connection. Be very careful not to overheat the connection, if it doesn't seem to be liquefying, back off and evaluate the situation before trying again.

Do you recall those vague allusions to 'strange hand grasps' at the start of this article? The time has arrived to reveal all as we delve into the arcana world of the Solder Suckers. No, these are not a lonely band of misguided consumers who bought the wrong type of solder, rather they are tools. There are three distinct classes, with numerous subspecies, but they all share the common characteristic of providing a small, rapid vacuum that pulls the molten solder out of the joint.

In true zoological fashion lets begin by examining the ancestral Solder Sucker which is by no means extinct. You can find this primitive at your local Radio Shack (Cat. No. 64-2086). All this tool consists of is a rubber bulb attached to a plastic tube that ends in a pointed hollow tip about the size of a circuit pad. Here's how to use it; if you're right-handed hold the bulb in your left hand and squeeze it closed (lefties use your right hand), you'll hear air being expelled out the tip, keep it closed. With the soldering iron in your other hand, apply it as described above. Once the solder is liquid, in one quick, smooth motion remove the iron and with your left hand place the tip of the solder sucker over the joint and immediately release the bulb. Let it spring open quickly, this will create a vacuum at the tip and suck up the molten solder.

Sounds simple, but the process is actually easier to describe than to accomplish on the first attempt. Two ways to overcome this; first find someone who knows how to do this and have them teach you, just watching is very helpful, or find a scrap printed circuit board and practice the technique several times until you master it.

The bulb type solder sucker is a very simple tool and has few problems. Occasionally it will fill up with bits of solidified solder and must be cleaned. To do this just hold it over a trash can and pump the bulb to blow the pieces of solder out, if they are too large to fall out then twist off the tip and shake them out of the bulb. The tip itself is made of a coated plastic and solder particles will not stick to it but can wedge themselves into the tube, use a thin probe (e.g. a toothpick) to poke them loose.

Proceeding up the evolutionary ladder of solder suckers we encounter the next great advance, the 'spring-loaded sucker'. In the Radio Shack catalog these are more politely termed Desoldering tools (Catalog Nos. 64-2098 and 64-2085.) Early on it was found that the bulb type is tiring if you unsolder many joints and also requires a well developed technique to work consistently. The spring loaded tool overcomes these problems. The tool consists of a tube similiar in shape to a syringe, inside there is a piston that is forced to the top of the tube by a spring. There is a fingerhold by which you compress the piston down against the spring until it catches on a trigger, in some designs the piston has a shaft extending from the top and this shaft is pushed down to prime the tool.

To use the spring tool first prime it by forcing the spring down, the designs vary so you'll need to figure out how to do this, most of the tools have fingerholds that allow this step to be done with one hand, allowing the other hand to hold and maneuver the soldering iron. Congratulations, you now know the strange hand grasps!!

Next you proceed as with the bulb type, heat the joint and when the solder is molten, remove the iron, place the desoldering tool over the joint and release the trigger. The spring will snap up creating a short, sharp suction at the tip. The spring creates a better suction than the bulb type is capable of, which makes this tool ideal for working on modern double sided boards.

Maintaining a spring sucker is much the same as the bulb type in that every so often you'll need to clean out the accumulation of solder bits. After doing
several connections there may be so much solder inside the tip that it will
jam up the internal piston, to clear this
I use a small piece of stiff wire with a
tiny L-shaped bend on the end, poked
up into the tip the edge of the wire
snaps the solder and pulls it out. At
some point you'll need to disassemble
the tool for a complete cleaning. Be
sure to follow the directions, it can be a
tricky procedure and due to the spring
you could end up with bits of solder
flying about if you make a misstep.
Brush off the internal parts, lubricate
only as directed and then reassemble
the unit. One problem with this tool is
that the tip will often become distorted
due to heat from the solder joints,
sometimes you can reshape it by
careful scraping and cutting, but even-
tually it will need replacing, and
replacements are available especially
for the more expensive of the breed.
A final word about the spring type
sucker, try to purchase a good quality
model, spend a little more than you
really want to. It's been my experience
that the inexpensive variety tend to
self-destruct after a short period and
you're faced with buying another one.
better to spend a little more to start
with and save in the long term.
Now let's move to the king of the hill
in solder suckers. These are really in-
dustrial models that most part time
electronics hackers would not have, but
it is useful to know they exist. Basically
these are little self-powered vacuum
cleaners attached to hollow tipped
soldering irons. They range in price
from a couple hundred dollars up to a
thousand or more. They are very effec-
tive and often come with a bewildering
array of accessories that make them at-
ttractive to electronic assemblers faced
with many solder joints to be un-
soldered.
Before we leave the topic there does
exist a sort of "missing link" in the
species. It can be found in the Radio
Shack menagerie (Cat. No. 64-2061.)
This is a soldering iron with a right
angled hollow tip that leads to a metal
tube at the end of which is the old
familiar solder sucker bulb. The bulb is
positioned such that it can be operated
by the same hand that holds the iron.
The advantage of this tool is that the
hollow tipped iron heats the joint and
the bulb then sucks up the molten
solder without the hand swapping
exercise inherent in the bulb or spring
type. It resembles the industrial model
in its basic principle.
At first appearance this would seem
to be the ideal tool but in my experien-
tce it has some drawbacks. First, the
iron has a hefty wattage rating (45
Watts for the Radio Shack model) and
can cause damage to circuit traces if
you're not careful. Secondly the tip is
usually wider than the standard pad
size of a circuit board, this forces you
to slide the tip until one part makes con-
tact with the protruding lead, unfortu-
nately this often interferes with com-
plete cleaning of the circuit board hole.
And lastly, it can be rather awkward to
operate the bulb, you just can't seem to
develop as much vacuum as with the
simple standalone bulb and nowhere as
much as the spring type tool. There is
one instance where this tool is helpful,
it occurs when attempting to desolder a
DIP and will be discussed later under
the mechanical aspects of unsoldering.
And for those who may not believe
solder suckers evolve, a new member of
the family has been sighted in the last
year or two. This type combines the

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spring loaded sucker with an iron. It's so new that Radio Shack hasn't captured one yet. OK Industries, Inc. lists one in their catalog (Model SA-6). This looks as though it might work well, it would seem to handle easily, has a small tip opening, and the snappy action of a spring sucker. Only drawback is that these tools cost between $25 and $35 depending on where you spot it, this would make it a tool for the 'serious' unsolderer.

Having covered the major types of solder suckers we move on to the other method of removing solder, wicking. Because we make the solder into a liquid by heating it, we can also wick it up just like a candle wick or a kerosene heater wick. For solder we need something that solder will stick to in order to achieve the wicking effect, this is accomplished by using braided copper that is flattened into a band.

Using solder wick, or desoldering braid as it is sometimes called, (Radio Shack Cat. No. 64-2090) is easy but there are a few tricks that we'll mention. Take the solder wick and unroll a few inches of it, hold it in your left hand, with the iron in your right hand. Place the wick over the joint, and then place the iron onto the back of the wick right above the joint. Now let the iron heat the wick which then heats the joint. When the solder in the joint becomes molten it will be wicked up into the heated braid. When this happens the wick will turn silvery, you can see this happen. Let it stay for a fraction of a second to Wick up as much solder as possible and then remove both wick and iron, and presto! you're done — most of the time that is.

The technique of wick desoldering is very easy to learn but there are several technical facts that affect the outcome. First of all, after desoldering a joint, you must then cut off that part of the wick that is filled with solder, use your nippers to do this, solder wick just won't Wick after it is filled with solder. Always work with a freshly cut edge. You can buy solder wick in several different widths from some electronics supply companies because the width is important. You want to use a wick that is as wide as the solder fillet you're trying to unsolder. Too narrow a wick won't absorb all the solder, too wide a Wick risks damage to traces from the extra heat it holds. The width sold by Radio Shack is a good all around size but if it's too wide for a small joint then trim the end a little.

The wicking action is not as vigorous with solder as with other liquids thus sometimes you may find that solder will remain in the circuit hole or on the opposite side of the board, especially double sided boards. This can also happen when using a solder sucker, although a good spring-loaded type will tend to preclude this. No problem though, just apply the wick on the top side, being careful not to let the side of the iron char components. Many times a stubborn joint will respond to the use of a desoldering tool followed up by solder Wick.

Occasionally, you will create a solder bridge between two traces, these rascals are very insidious, causing many problems not the least of which is how to correct them once you identify the culprit. The old method involved using a cleaned iron, which is then placed over the solder bridge causing the solder to creep up the iron by capillary action thus removing the bridge. That sounds much better than it actually works, it requires good technique. Instead use solder wick, just place it over the bridge, heat the wick with the iron and bingo the bridge disappears into the wick. This works so
well that sometimes you will have go back and resolder the joint because too much solder went into the wick but it still beats the old soldering iron method. I would be sure to include some solder wick in my tool kit if for no other use than removing solder bridges.

One final note, as in all soldering and unsoldering, never leave a heated solder wick on a connection too long, if the process doesn't work in a few seconds then there is something else wrong and leaving the iron in place longer will only damage the circuit board or the parts you're working on.

**Helping Hands**

One basic rule: if you're trying to unsolder parts from a circuit board that board must be firmly fixed in place. If you're unsoldering a wire from a lug then the lug must be firmly fixed. It is virtually impossible to hold something down while trying to unsolder from it and equally impossible to unsolder moving parts, so you have to employ some type of holder to fix things in place. One tool is Radio Shack's "Helping Hands" (Cat. No. 64-2093), you can also use small vise such as those sold by Radio Shack.

All these bits and pieces can start to add up, especially if you only want to do occasional unsoldering. To solve this, I made my own simple type of "Helping Hands", all I did was cut two pieces of white pine (any other wood will do), one piece is the base and measures 4' wide by 9' long and is ¼" thick, the other is 2 ½" wide, 9' long and ¼" thick, all the dimensions are variable, use whatever stock you happen to have. Glue and screw the narrower piece to the wider one along the 9' length to form a right angle. Now take two wooden spring type clothespins and drill one or two holes in one leg of each, use small wood screws and a little glue and attach these to the narrow piece spaced about 4" to 5" apart. This device will hold a circuit board up for inspection and work and you can also leave the board in it and tip it over so that the weight of the holder keeps the board from sliding about. Let your imagination run wild and make similar holders for whatever size and shape of boards you work with.

**The Art of It All**

Having talked about the unsoldering tools of the trade we now arrive at the larger issue of the art of it all. Knowing when to unsolder, how to unsolder, how to avoid damage, when to deliberately cause damage.

It is now time to talk about the special theory of unsolderability. What, you never heard of this theory before? Well that's because I just made it up. No, seriously we can express this as a formula, where U equals the degree of difficulty, P equals the number of pins to be unsoldered, thus we have:

$$U = P^2$$

or to express it in literal terms the more pins or leads a given part has the more difficult it will be to successfully unsolder it, without damaging the part or the board.

Actually this theory is not my own invention and you may have seen it expressed before but in its inverse form. Some kits or articles will tell you that the best way to unsolder something such as a 16 pin IC DIP is to use a small nippers and cut all the leads, freeing the IC from the board, then unsolder each individual pin. Naturally this works quite well from the boards perspective but leaves a useless IC.

This is where the art comes into play. If an I.C. is defective then there is no reason to protect it, and you should instead try to protect the circuit board.
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On the other hand perhaps it’s the IC that you wish to recover and the circuit board that is useless (obsolete or damaged, for instance). You'll have to make a judgement about this issue each time you unsolder anything.

Backing away from multi-lead IC’s, it becomes obvious that the easiest thing to unsolder is a single connection part, typically only wires fall into this category. You can use the destructive technique of clipping the wire and then unsoldering the stub. Let’s spend just a moment on this method. The advantage is that you can unsolder a stub by simply holding the circuit board in one hand, apply the iron, and when the joint turns molten, rap the edge of the board against something solid such as your workbench and the stub will just fall out, most of the time, stubborn stubs are not unknown. No need to bother with solder suckers, etc. You may need to use a little solder wick to clean up the pad on the board, but rarely so.

This is the great appeal of the destructive method, you lose the part but preserve the board and it’s convenient.

Returning to the example of unsoldering a single wire, many times we won’t wish to cut the wire because it will become too short, also stranded wire leaves a stub of many little wires that are actually difficult to tap out of the hole. In this case, it is better to use a solder sucker and remove the wire intact. After using the solder sucker you will find two possible conditions either the wire will pull right out with gentle tugging, or it won’t. The reason it may not is that enough solder is left in the connection to hold the wire from releasing. More art now is required, you may be able to free the wire by using some solder wick and wicking out the remaining solder, typically this should only be necessary if you really didn’t do a good initial job of sucking out the solder, perhaps you didn’t use an iron sized to the joint, maybe you used a bulb type sucker and not enough vacuum was developed, maybe some solder remains on the opposite side of the board. Another possibility is that wire is a very tight fit to the hole and even a small trace of solder will keep it held fast. In this case, what you may need to do is to fix the board in a holder, grab the wire on the opposite side with one hand (far enough away so you won’t be burned) and apply the iron while you pull gently, as the iron heats the bit of solder it should lose its grip on the wire.

Parts with two or three leads are very common. To unsolder these just remove solder from each lead as described above. About half the time the part will fall right out, and half the time it won’t. Wiggle it, this should break any weak remaining solder bond and then the part will come out. If it wiggles freely but still won’t come out check for solder blobs at the end of each lead, to remove any just wipe the iron across the lead, most of the solder will adhere to the iron and leave a smooth lead, for really stubborn blobs use solder wick. Check for any residual solder on the top side of the board. Rarely, the part still won’t pull out, this is usually because the part was tightly fitted originally and friction is holding in it, just keep proying at it firmly but cautiously.

Unsoldering A Dip

The vast majority of digital IC’s come in DIP packages, the word DIP is an acronym for dual in-line pins, the smallest has eight pins, four to a side, the largest 84 pins, 32 on each side. Parts with two or three leads are easy to unsolder and even if damaged in the process most are readily available and inexpensive. IC’s fall into a middle ground since some are as common as resistors, others harder to find and some almost impossible to replace, and the number of pins is irrelevant in this regard. There are companies that make a living by recovering out-of-production IC’s from circuit boards, testing them and re-selling to those who still need them for repairs, etc. Those firms don’t care what happens to the boards, they want the IC’s, generally we’ll want to preserve the board and perhaps the IC. By the way, resistors, capacitors, etc. have leads, IC’s have pins or sometimes legs but all three terms are used interchangeably.

If you don’t want to save the IC then the best approach is to cut it off the board. Clip each leg as close to the IC body as possible, until the body falls away. Next get a small spring loaded heatsink, forceps, or tweezers with a rubber band to clamp it shut, invert the board and clip this tool onto a pin so that gravity aided by the tool’s weight is acting to pull out the pin. Heat it with an iron, being sure to press the iron evenly against both trace and pin but don’t wedge the pin, use a gentle touch. You want to be sure to contact both so that on double sided boards the heat will be transferred to the trace on top. Once the solder liquefies the pin should fall out. Repeat for each pin.

There is a variation that requires a little more dexterity and doesn’t always work smoothly. In this method you omit the heat sink or forceps and just heat the pin until the solder liquefies then tap the board against the edge of your workbench and it should pop out. Alternately some people blow the pin out when the solder liquefies.
this requires dexterity and practice, and as a hot soldering iron is involved, care must be taken. Occasionally the tip won’t come out because it has wedged, so it is important to try to blow out any more as you’ll risk damaging the board by repeated attempts. Instead use the first method described above.

Suppose that you want to get the IC out intact. The first step is to remove the solder from each pin exactly as you would for any other component. Use a solder sucker, bulb or spring type, and as in the technique above be sure that the heat transfers to the opposite side by butting the iron against both pin and trace.

Regardless of whether you’re trying to save the IC or not, be careful not to overheat each joint. Overheating can damage the printed circuit traces, especially the small ones that cluster near IC’s. Hold the iron no more than four, possibly five seconds tops. To measure time, use a counting technique, such as saying One-Mississippi, Two-Mississippi, etc. It takes almost exactly a second to say each iteration, and any multi-syllabic state name will do such as One-Massachusetts, etc. Just don’t alternate back and forth as that will confuse both time and geography, a sure-fire route to the Twilight Zone.

Allow a little time between the pins so that the IC can cool internally. Once all the pins have been cleaned, carefully inspect them (a magnifying glass is helpful). Look for a dark half-moon in each hole, this indicates that most of the solder has been sucked out. If the hole still looks shiny then solder probably remains in it. To fix this don’t try to suck it out, instead re-solder the joint, that’s right re-solder it then try to suck it out the solder normally. For some reason it works much better this way. If it still comes out shiny try wicking the solder out. As you work on a recalcitrant joint there’s a terrific natural tendency to try too hard, to apply the iron too long, to press too hard—remember this and strive to avoid it.

Let’s step back for a moment and take a look at an IC before it’s inserted into a board. The pins will typically splay out, and to insert it they will have to be compressed or bent in. Once in position they will splay out again, pressing against the side of the hole which on double sided boards will be plated (the trace extends thru the hole to the other side). No matter how well the solder is removed a small residue will be trapped between the pins and the side of the hole. Individually this is a weak connection and in the case of small two lead parts this can be broken by just wiggling the part, but with DIP’s each leg (either 14, 16, 20, 28 on up) contributes to the whole and will keep that IC firmly anchored in place.

To free up these ghost connections will require a simple tool and a moderately difficult procedure. Go out and buy a set of cheap jeweler’s screwdrivers, yes cheap because they won’t be used for driving screws, instead they will be employed as probes. Feel the ends of each screwdriver and if sharp then round them a little with a file, just enough so that they won’t scratch if slid along on a circuit board. Now using a screwdriver with a blade just about the width of an IC’s pin, place it against the board next to an IC pin. Holding it down so it won’t jump over the pin push gently inwards with just enough force to break the weak solder bond inside the hole. You’ll know this has happened because the pin will suddenly give way and then you’ll see that it can be wiggled back and forth inside the hole. This technique requires some care and a lot of practice to learn and practice just the appropriate amount of force, but it is necessary. Without this step you’ll probably be unable to pry the IC off the board without breaking the IC and lifting some traces.

Sometimes as you attempt the first few pins on an IC you find that too much force is needed and instead of freeing the pin the screwdriver slips over it perhaps even bending it down, when this happens I use an extra step that usually helps. Take some solder wick as thin a width as you have and place it next to the pin just like the screwdriver blade then with the hot soldering iron press in gently. Let the heat of the iron do most of the work.

The pin should move inwards in a few seconds and the wick will pull out the residual solder. After this the screwdriver should easily free up each pin. So little solder is involved that you can slide the iron along the wick as you do each pin and do several before trimming the wick is necessary.

Once each pin has been freed, turn the board over and using one of the

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280 CP/M compatibility required.
larger screwdrivers as a lever pry alternately under each end of the IC. If everything goes right the IC will resist a little at first and then suddenly lift right off, and you'll find little or no trace damage. Sometimes there won't be any way to use a screwdriver to get under the IC because of crowded conditions, you'll have to use your imagination to come up with some tool that will do the job, some commercial IC pullers made for removing them from sockets can be used in this instance, although it will be a little awkward.

The above technique takes only a little more time and effort than the cut and destroy method. It can save a valuable IC or at least allow you to test an IC after removal to verify that it was or was not malfunctioning. It's worth the effort to practice it but there are no sure things in IC unsoldering, sometimes it's a snap and other times it's the IC that 'snaps'.

To summarize: Step 1) Remove as much solder as possible using a desoldering tool. 2) Free each pin using a small screwdriver as a probe. 3) If step 2 won't work use some solder wick and a hot iron to clear any remaining solder, repeat step 2. Single sided boards will usually respond to just the first two steps, double sided boards due to the plated thru holes will often require the full treatment.

Snags and Snarls

There are many things that can conspire to defeat your best efforts to unsolder a DIP, fortunately only one or two problems will rear up at one time. If you can identify the problem then you may take steps to overcome it. What follows is a rogues gallery of the most common ones.

By far the most frequent trouble is caused by residual solder that bonds the IC firmly into position. You know this has happened when you try to free the lead or pry the IC off the board. There is a fine line between the amount of force needed to remove an IC that is going come off cleanly and one that is stuck in place, exceed that line and the IC will break or some traces will come off with it. Practice is very helpful to allow you to estimate the force needed but experience is indispensable because boards are different, what works on one will fail on others.

Here are some hints on defeating the residual solder dilemma. Always inspect the top of the board, look for solder fillets around the IC pins, double sided circuit boards with plated through holes are the prime culprits. Proper unsoldering techniques should remove these bonds but occasionally one or more will remain. Refer back to the procedures outlined above and try to resolder and then unsolder these joints. If you find quite a lot of them, then an alternative method may help. This involves using an unsoldering tool that contains a built in sucker (remember the 'missing link'), place the tool over the joint let it liquefy the solder then slide it a little to move the IC pin around while releasing the bulb or trigger to suck up the solder.

The above method also works on circuit boards that have small hole diameters, where the standard procedure is not adequate. Once in a great while you may encounter boards with such tiny hole diameters that nothing may work. At this stage we should point out that whenever something is soldered together there is the implied assumption that it will never need to be unsoldered, some designs can make it very hard indeed to safely unsolder an IC. If you absolutely must accomplish such a task there is a tool that may do it for you. It's an accessory for certain soldering irons that is shaped so that it contacts all the pins of an IC at one time. It's not inexpensive and you need one for each IC size and above the 18 pin size it becomes rather impractical, therefore we'll only note its existence without a detailed examination of this tool type.

There exists yet another reason why certain IC pins may contain residual solder. Any pin that is connected to a large trace such as a ground or power pin will prove difficult to unsolder. Using a larger tip on the iron will often help as well as using solder wick. Sometimes these pins may resist all effort to remove the solder, but there is usually only one or two per IC. These are easy to deal with if you have three hands.

Seriously, the third hand is really any sort of holder for the board that lets you reach around to the component side with one hand and apply the soldering iron with the other hand. The scrap wood holder described earlier or a commercial product work equally well. Since you'll be grasping the IC by its plastic body you should be able to use your fingers, and if the body gets hot as to burn your fingers then you've overheated the IC. You could also use a tool to grasp the IC, the Radio Shack IC Extractor is fine or you can make one from scrap metal by studying the basic idea. Grasp the IC so that you can exert firm but even pull on it, apply the soldering iron to the recalcitrant joint and as it liquefies start pulling. Even when two stubborn joints are involved if they are on opposite sides of the IC then you'll be able to free one side and then do the other. Afterwards you'll need to bend the leads back into shape.
but that's easy if you work slowly. The same method can also be used with stubborn resistors, capacitors, etc.

**Crimping and Staking**

Some circuit boards needing repair may have been assembled using these industrial techniques. When the components are mounted on the boards by hand prior to a one-step soldering process such as dip or wave soldering, crimping and staking serve to keep the parts from falling out while in process. Unsoldering those parts later can prove extra difficult.

Crimping is done to small two lead parts, a special forming tool bends the leads so that when the part is inserted a high degree of friction with the board holes keeps it in place. To remove these parts is not too hard, you may need to wiggle them about a little more vigorously and occasionally the three hand method helps.

Staking is done to IC's, two or more leads usually at diagonal ends are bent over flat against the pad to lock the IC into place. Once soldered, the staked pins are really tough nuts to crack. To deal with them first proceed to unsolder all the pins as you normally would, in the case of the staked leads remove all the solder you can from on top of them. Because of the staking they will still be firmly anchored. To free them mount the board in your holder and take a small X-acto® or utility knife in one hand and your soldering iron in the other. Apply the iron over the pin, holding the knife so that you can slit it under the staked-over pin as the solder liquefies. Merely slip the knife under and lift a little, remove the iron, wiggle the knife slightly so the solder bond doesn't reattatch. Use the greatest care when attempting this procedure, you'll be using a sharp knife and a hot iron, plan your moves so that if you slip neither knife or iron is likely to injure your hands. Absolutely wear eye protection, even better wear a face shield if you have one. Next take a small screwdriver and very slowly bend the lead into an upright position, bend too fast and the lead will fatigue and break. Use a good desoldering tool to clean out the hole (you may need to use the techniques described for stubborn joints).

For various reasons staking is becoming rare so you may never encounter these problems. Since it makes unsoldering so much more difficult you'll appreciate why we don't encourage its use when assembling a kit. Even the cut and destroy method of IC removal is partially ineffective against staked leads.

**The Isolated Pad**

This is hard to categorize, is it an unsoldering problem or a board repair problem? Certain IC's do not have all their pins connected to the circuit, the pad for those pins will have no trace leading away from it, it's isolated. If

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**Figure 7:** Rough sketch of the 'scrap wood' helping hands described in the article, all dimensions and materials can be modified as needed.

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you try to unsolder one of these leads, you may find that the pad will delaminate, it may even be sucked right up into the desoldering tool. This is especially likely on single sided boards. The best news we have for you is, it's not your fault. Isolated pads simply can’t dissipate heat very well, without a trace heat is confined in the pad and quickly delaminates it. On double sided boards the plated thru holes help anchor the pads in, and sometimes there is a trace but on the top side under the IC where it can’t be seen.

Of course the pad isn’t actually necessary, it’s there because the pattern used to make the printed circuit included pads for all the leads. You only need to repair an isolated pad for appearance sake or if you need to attach a jumper to the lead, a pad facilitates that. And in the next article we discuss glue-on pads that make such a repair rather easy to accomplish.

To Socket Or Not To Socket
That Is The Question.

With apologies to the Bard of Avon, this brings us to the matter of using sockets or eschewing them. You may ask why discuss this in an article about unsoldering things, the reason is very simple—if you control how something is put together then you also affect how difficult it may be to take it apart.

Looking at commercially produced circuit boards, you'll notice the distinct lack of sockets. There is one very obvious reason for this, sockets cost money and for companies producing large numbers of boards omitting them adds up to big savings. For the casual builder the cost of sockets is almost insignificant especially when weighed against the convenience they provide. From this it might seem that you should always use sockets, some people including myself would take this position, others hold opposing views and it may be worthwhile to delve a little deeper into the reasons for the controversy.

There are some additional and subtle reasons why commercial builders omit sockets. If you read the article on soldering you may remember the part about how corrosion affects all metals. It happens that soldering a connection almost entirely eliminates that danger, a soldered joint is considered in the trade ‘gas-tight’ that is unaffected by the oxygen and other contaminants in the air. Wire-wrapping, and some other connection methods also produce gas-tight joints. But the junction between an IC’s pins and a socket’s metal are not truly gas-tight. When sockets first came into use they were particularly prone to this problem and earned a reputation for unreliability.

Socket manufacturers responded to this by making a whole series of improvements. They designed better contact points, and then they re-designed them again, until today each socket maker trumpets the effectiveness of their design. They began to gold plate the contact points, gold being extremely resistant to corrosion. All these steps taken together have achieved modern sockets that perform equivalently to a soldered connection. Yet the early problems still influence some builders, who may not use sockets at all. Others use only a certain brand, or especially avoid some brands.

Buying from a reputable source should result in a good quality socket, always choose the gold plated type as the small extra cost is worth it.

Now we circle back to a very subtle reason for omitting sockets, directly related to unsoldering. Commercial assemblers have available repair

(Continued on page 33)
Analog Data Acquisition and Control
Connecting Your Computer to the Real World
by Jerry Houston

Analog Fundamentals

After my first data processing class in college (Business BASIC, of course), I felt ready to take over the world. Nothing could stop me now - I could make a computer do anything! (Most readers can probably remember the feeling.) The shock of reality came a little later, while I was still in school, and working for a security company.

My supervisor occasionally installed some fabulously expensive solid-state security systems, and it occurred to me that a computer like the Commodore 64 (just then becoming really affordable) could make all the decisions needed for commercial security. I was right, of course, but nobody talked with me about how to make the computer respond to inputs from security switches. I was too busy with full time work and full time school to take on an additional study of electronics, and begrudgingly let go of the idea.

Actually, I gave up doing anything about it at the time, but I couldn’t quit thinking of fascinating projects that would be possible if my computer weren’t limited to working just with its peripherals. I had no shortage of ideas - just no way to make any of them work.

I was the kind of person who passed over magazine articles and advertisements dealing with analog/digital electronics. I’d never had the opportunity to learn much of the terminology involved, and dismissed the subject as something that would probably only interest a technician anyway. But when my business and computer training landed me a non-technical job in a company that makes A/D converters, I managed to get myself hooked, but good!

For the totally unininitiated, as I certainly was, I should explain that analog/digital conversion provides a way for any computer to “talk with” the rest of the world – the Real World that exists outside the computer. There are analog/digital data acquisition and control systems… whew! let’s call them “ADCs” from now on… available from a number of companies, that not only let the computer MONITOR its environment but allow it to CONTROL its environment as well.

These ADCs provide digital controlled outputs that can be used to operate relays or indicators, and some even come with a transmitter to operate the familiar BSR X-10 series of AC line carrier remote control modules. These are the modules available at Radio Shack stores and elsewhere, that are turned on or off by radio-frequency signals sent over the AC lines. Once a computer can operate BSR modules, a programmer can easily design a sophisticated home control (and energy management) program using convenient wireless remote control.

Most computers that we know of today are DIGITAL computers. They respond to electrical impulses that are either there, or they aren’t. Bits in our computers are either ON or OFF. By bunching those bits into bytes and words, we can express larger digital values, but information in a computer can all be traced back to those two values, ON or OFF. Most of the important information in our environment comes in ANALOG form – it varies continuously along a scale of some kind, and has meaning only because it relates to some observable physical phenomenon that we can perceive.

If we hear from an astronomer that the normal temperature on the surface of a certain planet is 300° Kelvin, most of us would have learned very little. If the same astronomer then says, “by the way, water freezes at 273° K and boils at 373°K”, suddenly that first statement means a lot more. Without doing a lot of tricky math, it’s easy to see to that 300°K is comfortably above freezing, and a long way from boiling – phenomena that we all understand.

Analog information is everywhere we look. If we glance at the speedometer, we don’t want to know whether we’re moving or not, we want to know “how fast”. It doesn’t help to know that there’s water in a swimming hole – before we dive in, we need to know “how deep”.

A computer can do some remarkable things if it’s able to gather analog data from its environment, and if that data is converted into a digital form the computer can deal with. Knowing “how dry” the lawn is, the computer can decide when to turn on the sprinklers. Knowing “how bright” the sun is, the computer can open the drapes in the winter, or close them in the summer. “How fast” a drill is sinking into the ground can be important to an oil well driller.

Actually, there are few fields of endeavor that can’t benefit from computer automation, now that chips are cheap and – you guessed it – easy-to-use ADCs are available. These devices take care of the details of interfacing a computer to the Real World such that a programmer can concentrate on writing code to solve the problem at hand. He doesn’t have to become expert at digital electronics.

Another Peripheral

ADCs come in various packages. Some are printed-circuit boards, designed to plug into a bus slot of a particular computer. Others are external devices that look a lot like a modem. Because the internal variety link directly with the data bus of the computer, they often provide faster operating speeds than external models.

External ADCs usually communicate with the computer via the RS-232 C serial interface. One advantage to such a device is that it can be located close to the sensors being read, often hundreds of feet away from the computer, as the length of certain kinds of sensor leads can be critical. Another advantage is that the external ADC won’t require a particular computer to operate it. Communicating with standard ASCII characters, it’ll work with the computer at home, the one at the office or lab, or the new one you get next year.

Regardless of its type of mounting, ADCs can be intelligent or operate according to discrete (fixed) logic. Models
are available that can acquire data at high speed and store it in an internal buffer, releasing it to the computer later for processing. Other kinds communicate with the computer in real time, passing along each reading as it is taken, and all the processing intelligence resides within the computer.

The Hardware
With ADC systems, like any other product, the cost of the device depends a lot on the features that it offers. Since most ADCs do pretty much the same thing, the range of prices depends more on how they do it. The main considerations are:

1. Number of each kind of input or output.
2. Resolution (accuracy) obtainable.
3. Speed of analog sampling.
4. Additional features, such as BSR controller, analog outputs, memory buffers, etc.

As in making any other kind of purchasing decision, the prospective user needs to analyze his needs and then to buy accordingly.

The very simplest ADCs offer as few as 4 or 8 analog inputs and little else. In fact, they're not really ADCs, because the "C" is missing -- the control function. They're useful for monitoring analog sensors and laboratory apparatus, where the purpose is to log data to a printer or disk file.

The next step usually includes some number of digital outputs as well as analog inputs. With such an interface, a computer can monitor the temperature, for example, and turn on a furnace or air conditioner when it's needed.

If the ADC also includes digital inputs, the computer can monitor ON/OFF conditions such as security switches or event counters. Since digital inputs can be monitored directly by a computer, an ADC isn't really needed for this particular job, but digital inputs on an analog interface are pretty handy anyway. If the computer controls a home environment, it needs to monitor analog parameters (light, temperature, moisture, sound, and so on), and if digital inputs are available the computer can handle the security needs as well. The ADC will pass properly conditioned digital values to the computer in a manner that makes programming easy.

Two other features are sometimes useful, the BSR transmitter already mentioned, and D/A outputs. D/A conversion is the reverse of A/D, naturally, and means that the ADC will output an analog voltage that represents a digital value passed to it by the computer. D/A outputs can be used to control the speed of motors, operate chart recorders, or provide a variable-voltage for other special purposes.

Resolution has to do with the accuracy and sensitivity of the analog inputs, and ultimately with the cost of the equipment and the speed at which it can operate. Since the resulting digital value is sent to the computer as a collection of bits that are either ON or OFF, a convenient and common way to refer to the resolution of an ADC is in terms of the number of bits used. ADCs are commonly available that provide 8-bit or 12-bit resolution, and not-so-commonly with 16-bit or higher.

The size of each increment of resolution has to do with the overall range that can be read by the inputs (represented as a voltage) and the number of bits of resolution. Since each bit can represent only one of 2 conditions (ON or OFF), 8-bit resolution provides 2 raised to the 8th power, or 256 increments. 12-bit resolution offers 2 to the 12th, or 4096 increments. If the ADC is also capable of determining polarity, then it can differentiate number of increments to either side of zero (doubling range with the same resolution).

To put resolution into perspective, consider that you might want to monitor and control the temperature of a heat-treating furnace. Your sensor might be one that operates over a range of 0 to 2000°F, and you probably would have the ADC input well matched to the sensor, so that none of its range was wasted. With 8-bits of resolution, each increment you could measure would be nearly 8° wide. With 12-bit resolution, the increments would each be less than 0.5°, and with 16-bit resolution (1-in-65,535), you could be accurate to within 0.03°.

I promised you that resolution has something to do with both cost and speed, didn't I? Well, as you might expect, 12-bit systems are generally more expensive than 8-bit systems and usually slower. How slow? Even inexpensive 8-bit systems can often read thousands of samples per second. If your application requires that kind of speed AND your computer and language can do something with infor-
mation coming in that fast AND you
can get by with 1-in-256 resolution, then
consider one of the fine 8-bit systems
that are available. Other factors being
equal, it will cost the least.
If you need more resolution, 12 bits
will provide 1-in-4096, as mentioned
earlier. Most 12-bit ADCs on
the market will sample at speeds from 5 or
so up to 100 samples per second. As
with all the other factors, your ap-
application is important here. To digitize
speech patterns might require many
thousands of samples per second, whereas
someone monitoring the movement of a
glacier might be well
served by one measurement a week!
Some ADC systems are made up of a
basic circuit board to which optional
modules can be connected, thus
providing a choice of the features men-
tioned above. Also, some manufac-
turers are responsive to custom orders,
and make options available when
needed. An ADC that normally includes
six controlled outputs and a BSR tran-
smitter is also available with twelve
controlled outputs and no transmitter.
People working in the field [with no
way to use BSR modules] sometimes
opt for the additional outputs.
Some companies offer the ultimate in
options, intelligent microprocessor-
controlled data acquisition and control
systems designed and built just for a
particular task. With no need for a
supervising computer, such systems
can be very inexpensive in reasonable
quantities. If your needs are of an OEM
nature, or your application requires a
number of systems, ask if one can be
built to your specifications.

Applications
The following applications are all
made possible by a data acquisition and
control system. In most cases, the
programmers involved are NOT com-
puter professionals, but specialists in
their own particular fields. They’re able
to program their computers to make
the necessary decisions, and that’s
pretty much all that’s needed. Some of
these uses for Real World computing
may suggest ways that an ADC system
could make your life easier!
(1) WSB-TV in Atlanta uses a Com-
modore 64 to collect meteorological
data from 13 remote weather stations
in the surrounding region to provide
detailed reports of current con-
tions. Each station consists of a modem, an
ADC, and sensors to measure wind
speed and direction, temperature, and
relative humidity. Every fifteen
minutes throughout the day, the C-64
calls the remote stations in sequence
and acquires up-to-the-minute
meteorological data. The Atlanta area
is characterized by small agricultural
areas that differ dramatically in
weather. Because of this system, far-
mers in each of these regions now have
access to vital regional weather infor-
mation not available until now.
(2) A southern California electronics
firm is using an ADC, controlled by an
Apple IIe computer, to perform final
manufacturing tests and calibration of
medical power supplies. All 16 of the
ADC’s analog inputs are employed to
measure voltages at test points under
various load conditions. The power
supply loads are applied systematically
via the ADC’s power line carrier
remote control system. The computer
is programmed to prompt the
technician when an adjustment is
required, or a unit is defective, and to
produce a printout of the final test
results. Even the clock frequency is
monitored via a frequency-to-voltage
converter. It is estimated that the
automated power supply checkout is
now accomplished in one-seventh the
time that a technician previously
required.
(3) Amana has reduced the time
required to test room air conditioners
from 20 minutes to only 2 minutes
through the use of an ADC monitored
by a Commodore 64 computer. The new
test, which monitors the rate of tem-
perature change and the amount of
current that is consumed in the process,
no longer requires a special testing
booth and dedicated equipment. It
requires less attention from
technicians, can be done on the factory
floor, and saves a considerable amount
of energy. The first system is working
so well that a second is being developed
to run concurrently. A third Com-
modore 64 is used to process the test
data in the DP department.
(4) A graduate student at the Univer-
sity of Southern California is using an
ADC and a Macintosh computer in con-
junction with a spectrofluorometer and
spectrophotometer to investigate the
optical properties of marine
phytoplankton in the Arctic Ocean.
Samples of natural populations of
marine phytoplankton are filtered and
then placed in the spectrofluorometer
or spectrophotometer for measuremen-
ts of fluorescence excitation and spec-
tral absorption. The output is directed to
the Macintosh through the ADC and
has the advantage of allowing real-time
display of the data, rapid processing,
and graphical representation.
(5) A former pharmacist, now a
quadraplegic confined to a wheel chair,
uses an ADC with an IBM-PC and
Keytronics 5152 Voice Actuated
keyboard to control and monitor his
environment. He uses the BSR X-10
transmitter feature of the ADC to con-
trol lights and appliances in response to
voice commands that he gives to his
computer. This is an early start for
him — soon he’ll be able to control his
heat pump, home security system, and
other devices in the same way. His suc-
cess in controlling his environment has
prompted him to develop and market
such systems to aid other handicapped
persons, using the Corona PC when
voice-actuation is required, and the
Commodore 64 as a lower-cost alter-
native for those cases in which the user
is able to operate a standard keyboard.
(6) A grower of miniature roses in
Seattle uses a Radio Shack Model 100
portable computer and an ADC to
monitor and control his commercial
greenhouse. Analog sensors monitor
soil moisture, soil and air tem-
peratures, relative humidity, and light

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levels. Watering and fertilizer application are automated by this system, as well as temperature and humidity control.

(7) The Environmental Engineering group at Louisiana State University’s Civil Engineering Department is developing a remote water quality monitoring and control system for use in soft-shell crab mariculture systems. The system is based on a Kaypro computer and an ADC. The objectives of the research include control and optimization of the molting process in blue crabs in totally enclosed aquatic systems. Real-time data on water quality and system status are collected by conventional laboratory instrumentation interfaced to the microcomputer through an ADC, monitoring pH, temperature, dissolved oxygen, light and water levels.

(8) An environmental consulting firm is currently studying the change in air quality resulting from air passage over two Seattle area sanitary landfills. Data logging and sampling stations consisting of an ADC, a NEC PC-8201 portable computer, a wind vane, an anemometer, and an air sample pump were installed at locations upwind and downwind of the landfills. The NEC is programmed to monitor running averages of wind speed and direction, and to activate the sample pump when the wind parameters are within appropriate limits. The environmental data as well as sample duration and time are stored in RAM. Each system is battery powered and uses only 65 mA for monitoring and 350 mA when the pump is activated.

(9) The City of Grand Rapids uses an Atari home computer and an ADC to monitor the cyanide content in the wastewater they treat. Cyanide is a pollutant that threatens the microorganisms used to treat waste, and wastewater that contains cyanide must be shunted into a special holding area from which it can be released later at a rate that will cause no harm.

(10) To study the formation of patterned ground in arctic permafrost regions, an ADC in conjunction with a Radio Shack Model 100 was used by University of Washington Quaternary Research Center scientists to monitor strain gauges and temperature sensors buried in the soil. Four systems have been deployed near Spitzberg in Norway to record stresses and soil motion resulting from the annual freeze-thaw cycle. The soil surface in this area is marked by a distinct pattern of stone circles thought to result from the slow overturning of the soil in a convection-cell manner. The systems are battery powered, and cold-room tested to -40°C prior to field installation.

(11) A fruit storage company in Washington State uses an ADC and a Kaypro computer to monitor and control special rooms in which apples are stored. Temperature, light, and relative humidity are carefully controlled so that the fruit will become dormant, but not die, and it can be brought to market months later in just-picked condition.

(12) Two professors in the Department of Urban Planning at the University of Washington are using an ADC with an IBM-PC to study the effects of lighting glare and solar radiation on personal comfort and energy management for new building design and construction. Subjects are placed in a room with a desk and lamp and asked to determine what level of light they find to be most comfortable to work with. The subject controls a hand-held device which, through the ADC, is used to brighten and dim the level of solar light emitted by an artificial ‘window’ in relationship to the level of internal light, while maintaining a total illumination constant.

(13) A sophisticated home control system has been developed by a Seattle resident, using an ADC and a Macintosh. The system monitors and graphs energy use, temperatures, basement moisture, and a variety of security sensors. When a possible security intrusion is detected, lights are turned on progressively closer to the source of the alarm, simulating someone approaching and turning on lights. If efforts to enter continue, a conventional alarm is sounded. Home temperatures are controlled to different setpoints according to time of day and occupancy.

(14) The maker of a popular brand of disposable diapers uses an Epson HX-20 and an ADC equipped with a moisture sensor to test new types of lining materials. This research should lead to more efficient linings for the diapers, and dryer babies.

Vendor Listing
A listing of suppliers that manufacture ADC equipment is provided for the convenience of those who want to
find out more about particular equipment. The usual authors' disclaimers must apply, along with my apology to any company that I haven't found out about yet.

The products that I have listed range from inexpensive to not so, and from simple to complex. I've included ADCs that are usable with a variety of computers, and left out some that are an accessory for one system only. It is therefore not a complete listing, but a very good place to start. Because many of the products are better known by name than the companies that make them, they're listed alphabetically by product name in the vendor list.

Program Narratives
Two programs accompany this article. Program One is written in Turbo Pascal® and measures the performance of ADC systems in terms of how fast analog channels can be read. Program Two is written in MTBASIC®, and lets the computer operate as a multi-channel data logger.

In each case, code is used that reads the analog inputs of the ADC-1 Data Acquisition and Control System from Remote Measurement Systems, Inc. of Seattle. This is a popular ADC system, and the one most familiar to the author, but it is not at all difficult to substitute channel reading instructions for an ADC from another manufacturer. In each program, the code used to acquire analog data is identified as to its function. In Program Two, the first seven tasks look very much the same, but each has its own unique values, making it necessary to repeat a similar routine seven times to read seven channels independently.

Program One (HISPEED.PAS)
Two characteristics of data acquisition and control systems that are often important to the user are (a), the rate at which analog data can be acquired and (b), the effective resolution available. Both of these parameters are easy to measure with a computer program designed to read the ADC.

HISPEED starts with FUNCTION ACD, which converts BCD (binary coded decimal) values from a clock card into ASCII decimal representation for display on the computer screen and for manipulation as decimal values by the program. This program was written to
run on a Kaypro 4-84, but many other clock cards provide BCD data for date and time values.

PROCEDURES GETTIME and GETDATE are specific to the Kaypro clock card as written, but could be changed to work with many others. If you already have a routine written to get the time and date from your system clock, just insert them in place of these. To convert these procedures for another system, refer to TASK 8 in Program Two, where the function of each port is commented in more detail.

FUNCTIONS GETDATA and ANALOGIN are used together to send a command to the ADC-1 and to get a complete byte back from it. I write these as separate functions, because GETDATA is also needed with functions that read the digital inputs and send commands to the BSR transmitter of the ADC-1 system. They could, of course, be combined into a single function. To see how they work, look first at ANALOGIN. Since it calls GETDATA, ANALOGIN must come second in the listing.

ANALOGIN is a function to which a channel number is passed as a parameter, and which returns an integer value (the reading from that channel) to the calling routine. Because the ADC-1 provides 12-bit resolution, the answer can’t be obtained in one 8-bit byte, and two are used. HighByte contains the 4 MSB (most significant bits) of the data plus other bits that indicate polarity, whether the A/D conversion is finished, and an over-range indication. LowByte contains the 8 LSB (least significant bits) of the analog value at the channel. Now it’s time to take a look at GETDATA...

GETDATA also uses a parameter, the byte to be sent as a command to the ADC-1. The answer byte is the value that is stored in GETDATA, as if it were a variable. This function first sends the ADC-1 its command byte through the serial port (port 4 for the Kaypro), checks the status port (port 6 for the Kaypro), and compares the status value to the Mask (bit 1 for the Kaypro). The status check is repeated until a complete byte has been received, then GETDATA is assigned the value read from the serial port. Needless to say, to use this function with other computers, replace the port and mask values used by the Kaypro with those required for your machine. Now, back to ANALOGIN...

<table>
<thead>
<tr>
<th>Model</th>
<th>Manufacturer</th>
<th>Features</th>
<th>Price</th>
</tr>
</thead>
<tbody>
<tr>
<td>PL-1000 Measurement &amp; Control System</td>
<td>Elektor Associates</td>
<td>16 analog inputs of 12-bit resolution plus sign, 16 digital inputs, 16 digital outputs. CMOS for low power use. Standalone operation possible with built-in BASIC interpreter and 8K RAM. Unit can accommodate up to 2 I/O boards (optional) for additional channels. RS-232.</td>
<td>$399 basic system. Expansion boards $229 - $499. Many system options.</td>
</tr>
<tr>
<td>O-3824 Remote Data Collection System</td>
<td>Quasirtronics</td>
<td>2 single-ended analog inputs, 4 digital outputs. A/D resolution 1 in 5000 or 1 in 2000 using BCD digits, sample rate is 7.5/sec. RS-232.</td>
<td>$495</td>
</tr>
<tr>
<td>WB-31 &quot;White Box&quot; Interface</td>
<td>Omega Engineering, Inc.</td>
<td>2 analog inputs of 12-bit resolution, 4 digital outputs. A/D sample rate 7.5/sec. RS-232.</td>
<td>$395. Other models to $2000+. Omega has a fine catalog of sensors, that is well worth requesting.</td>
</tr>
<tr>
<td>1232 Data Acquisition &amp; Control System</td>
<td>Starbuck Data Company</td>
<td>Microprocessor controlled, 8 analog inputs of 12-bit resolution. 8 digital inputs, 8 digital outputs. RAM to store 2000 data points of burst data.</td>
<td>$690. Analog 8-bit 8032 version is now $390.</td>
</tr>
</tbody>
</table>
ANALOGIN sends a command byte that represents the channel number 1, which tells the ADC-1 to start digitizing that channel. (The terminal strips are labeled 1-16, and people tend to think that way too, but the ADC-1 is expecting a value from 0-15.) The next step is to assign HighByte the value returned from the ADC-1 when the command 161 is sent to it. The bit that represents the binary number 128 is checked until it is a 0, indicating that digitizing is complete. Then it's safe to read the LowByte, with a command of 145.

Only the 4 LSB of HighByte contain data (the rest are status), so HighByte is ANDed with 15 to result in the variable MaskedHighByte. From here, it's a simple matter of multiplying the masked high byte by 256 and adding the low byte. One final check is made to determine whether the sign flag was cleared—if HighByte AND 16 is 0, then the value is a negative one and the appropriate sign is added.

The procedure to calculate the square of the sample mean is used with the following procedure that calculates the sample standard deviation. All the analog readings that have been taken were stored in the array Buffer[1..1000]. For the number of readings that were requested, these elements are summed, then divided by the number of readings to provide the arithmetic mean. This is the mean squared.

The standard deviation procedure is straightforward. It squares each individual reading, sums the squares and uses a very standard statistical formula to determine the standard deviation of the entire sample.

The program main logic should be easy to follow, as the variables, procedure names, and function names are self-describing. Basically, the program asks the user how many samples to read, checks the time, reads the samples and stores them to the Buffer[] array, then checks the time once more. The number of seconds required to do the reading is divided by the number of readings made to determine how many samples per second could be read.

The user can decide to display all of the samples to the screen for a visual check, or only a few of them. Pressing RETURN will display them all. The number of them that were displayed to the screen will be used to calculate the standard deviation. Another RETURN
Program Two (LOGGER.BAS)

The second program is written in MBASIC source code, and to run it must be compiled. Because of its size, it won't compile to memory, and you will need to use the DISK COMPILe option of MBASIC to produce an executable .COM file from it. The multitasking feature of MBASIC makes it easy to write a general-purpose analog data logging program like this. One of the nine available tasks is used to read the system clock and another is used to log data to the disk file, so that leaves seven analog channels that can be read, each according to its own schedule.

Many people are not yet familiar with using MBASIC (though it has been the subject of a review in The Computer Journal), so I'll mention as I go some of the things a new user will need to look out for.

Praises to its inventors for providing some Pascal-like features in MBASIC! Variables are defined before use, as in lines 100-146. Multi-line functions are

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supported in the latest version of MTBASIC, such as the one at 190-210 which converts BCD numbers into ASCII numbers (as did FUNCTION ACD in Program One). Lines 216-218 initialize an array of channel flags to zero, which flags will later be used to indicate which channel has just been read.

MTBASIC supports multiple instructions on a physical line, using semicolon separators, but using short lines like these leaves plenty of room for good documentation, and I'll recommend easier-to-follow code every time.

Significantly, the compiler does NOT translate the comma-boost, so there is no efficiency advantage to be gained by leaving them out, and no real excuse to pack as many instructions on as few lines as possible.

The statements at 225-230 create two strings that are the escape sequences to turn off the Kaypro cursor. These might need to be translated for another computer, or left out entirely if that feature is not supported. (Notice that it isn't possible to concatenate strings using the operator, as in most versions of BASIC. The CONCAT$ function must be used each time one string is added to another.) Turning off the cursor provides a cleaner screen display, and is recommended.

CAUTION — Remember to turn the COOKIE ON at the end of the program, or you may have to re-boot to get it back!

The actual purpose of the rest of the code is pretty obvious from the comments, so I'll concentrate on explaining the workings of MTBASIC, where it differs substantially from other versions. The first seven tasks are designed to read the analog channels of the ADC-1 system, and would certainly have to be modified to work with a different interface. They are so similar (except for variables that indicate the channel that was read and cursor positioning) that I'll explain only the first task, then go on to tasks 8 and 9, which read the clock card and write to disk.

Task 0 (zero) is the main logic of the program, and it actually includes all the code up to line 545, the STOP command. Line 251 uses the familiar OUT statement to send a value of 14 to port 0, setting the Kaypro serial port to a baud rate of 9600. Other computers accomplish this with different port numbers and values, or with an OPEN statement.
The ERASE statement in line 252 clears the screen. Since WINDOW 0 (used throughout the program so that the CURSOR position statement will work) includes the entire screen, the WCLBAR statements (as in 280) also clear the whole screen. In other cases, they could be used to clear just a single window while leaving the rest of the screen alone.

Line 253 selects WINDOW 0 for use until another WSELECT statement is given. Line 255 establishes that WINDOW 0 will exist from screen lines 0 to 23 and from screen columns 0 to 78. WFRAME defines the characters to be used to outline the screen, the first character used for the horizontal lines (top and bottom) and the second used for the vertical lines (sides). After the window is properly framed, it is redefined to be one character smaller in each direction, so the framing characters are protected from overwriting.

The next lines just print prompts to the screen, and ask the user whether to schedule each of the first seven analog input channels. If the answer for a channel is "yes", then the user is asked...

---

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**Note:** Requires Microsoft’s M80 & L80 assembler & linker to setup Slave BMON.

8” SSD Disk containing Master BMON, Slave BMON, CONSOL, BMONIO, CONSOLIO, and Users Manual...

---

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Program 2

PROGRAM HISEED:

(* This program demonstrates high speed analog data acquisition
 with an ADC-1 system. Rather than display data at once, it
 simply stores data to memory as it is read.

If needed, the readings are displayed as integer numbers,
and a calculation is made of the sample standard deviation. In
this way, not only the speed but also the resolution of a data
acquisition system can be tested.

)*)

LABEL Start, Stop

TYPE Str = String(80)

VAR I,J,Value,Code,Channel : Integer

TimeX : String(32)

Buffer : Array[1...10000] of Integer

StartSeconds, StopSeconds, Elapsed, Samples : Real

Element,ElementSquared : Real

SampleMean, SampleSum, SumOfSquares, SampleMeanSquared, StandardDeviation : Real

Count, Times : Integer

Function ACD(x : Byte) : Byte

(* Converts BCD byte into standard ASCII coded decimal representation *)

Begin

ACD := ((x Div 16) * 10) + (x - (16 * (x Div 16)))
End

Procedure SetTime(X : Str)

Var Minute,Second,Hundr,Sec : Integer

Hun : String(2)

Begin

Port[34] := 151
Port[323] := 31

Minute := ACD(Port[36])

Port[323] := 21

Second := (Minute * 60) + ACD(Port[36])

Str[Second,Sec] := Integer(Second)

Hundr := ACD(Port[36])

Str[Hundr,Hun] := Integer(Hundr)

Time := Sec + '.' + Hun
End

Function SetUpCharout(Charout : Byte) : Byte

Const StatusPort = 01 (* these values computer-specific *)

SerialPort = 41

Mask = 11

Var Status : Byte

Begin

Port[SerialPort] := Charout

Repeat

Status := Port[StatusPort] (* read value from status port *)

Until ((Status and Mask) > 0) (* check for full byte received *)

Getdata := Port[SerialPort] (* read value from serial port *)

End

Function AnalogIn(channel : Byte) : Integer

VAR HighByte,LowByte,MaskHighByte,MaskLowByte : Byte

Value : Integer

Begin

Value := GetData(Channel - 11)

Repeat

HighByte := GetData(11)

Until ((HighByte and 128) = 0) (* checks for digitizing done *)

LowByte := GetData(15)

MaskHighByte := HighByte and 15 (* mask off needed bits *)

MaskLowByte := (256 + MaskHighByte) (* combine for 12-bit value *)

If ((HighByte and 16) = 0) Then Value := Value + MaskLowByte

End

AnalogIn := Value
End

Procedure CalculateSampleMeanSquared

Begin

SampleSum := 0.8

For I := 2 to J Do SampleSum := SampleSum + Buffer[I]

SampleMean := SampleSum / (J - 1)

SampleMeanSquared := SampleMean * SampleMean
End

Procedure CalculateStandardDeviation

Begin

CalculateSampleMeanSquared

SumOfSquares := 0.8

For I := 2 to J Do

Begin

Element := Buffer[I]

SumOfSquares := SumOfSquares + Element

End

StandardDeviation := Sqrt(((SumOfSquares / (J - 1)) - SampleMeanSquared) / (J - 1))
End

Begin

(* Main Program Logic *)

Port[8] := 14 (* sets Kaypro baud rate to 9600 *)

for a label to describe the input and the number of timing ticks that must pass before that channel is read. You'll need to experiment with these interrupt tics to determine how to schedule the tasks. I haven't made any careful measurements, but 300 tics seems to be about a second with the Kaypro and a 4 MHz CPU. Other computers will vary greatly.

For each channel that is NOT selected, the value of TKSI is set to 31000, a big enough number so that the program won't spend time very often checking to see whether to execute that task. Line 353 turns on the channel flag for each channel that's selected—otherwise it stays 0 as it was initialized in 217.

The OPEN statement at line 380 sets aside an output buffer for the selected sequential file.

The next statement unique to MT-BASIC (well, actually it looks an awful lot like a FORTRAN format statement!) is at line 510 where it starts out FPRINT. This is a formatted print statement, and takes the place of PRINT USING. In this line the X values refer to blanks, the I values to integers, the S values to strings, and the Z at the end passes a carriage-return. With the Z at the end, the statement is like a Pascal WRITELN procedure, without the Z it's like a Pascal WRITE procedure, and doesn't move to the next screen line automatically.

The format string is followed by the variable list to be printed, in this case the channel number, a label to identify the input, and the number of tics assigned to it for timing purposes. When all seven channels have been thus identified for labeled as "unselected" by line 500 the program is ready to begin executing.

Task 0, the main logic, contains the RUN statements that determine when each of the other tasks will be executed. These RUN statements include the task number and the number of tics to use. The tics can be supplied as an integer constant or as a simple variable, but NOT as a subscripted variable (array). For convenience, the loop at 310-360 inputs the tic assignments as the array TKSI, but these have to be converted—one at a time—into the simple variables TIKSI through TIKS7 at 364-365. (Note that MTBASIC is not at all tolerant of variable names that start out the same as reserved words (like TICS), so variables like TICS1
The Computer Journal / Issue #21

couldn't be used. It IS ok to use
keywords entirely within a variable
name, so something like XTICS1 would
have been alright.)

That's about it for the main logic.
Line 540 uses the KEY function to
check for an ESCAPE key pressed
(ASCII value 27), and gracefully exits
the program (in that case remember to
turn ON the cursor if you've turned it
OFF). All that's necessary once the
tasks have been listed in RUN
statements is to keep Task 0 executing
until you're ready to quit. I did it here
by checking for a key pressed, but the
same thing can be accomplished by
using a line that does a GOTO to itself. I
think it makes more sense to provide an
exit from the program, so I just keep
checking for an input that signifies it's
time to stop.

As mentioned before, the first seven
tasks here are very similar. They start
with a TASK statement like the one in
line 640 and end with an EXIT
statement like the one at 870.

Because these seven tasks all acquire
data from the same peripheral, they
must disable interrupts while reading
that data. Otherwise it would be
possible for several tasks to be sending
conflicting commands to the ADC-1.
This is accomplished with the INTOFF
(interrupts off) in 655 and the INTON
(interrupts on) in 865. The rest of the
analog-read logic is identical to that
used in Program One, except that these
tasks also contain some code to print
results to the CRT screen.

Where line 870 says CURSOR 9.3 it
is positioning the cursor within the
WINDOW being used, not the entire
screen. In this program it amounts to
the same thing, but the distinction is
important in programs that use several
windows. Again, the processing logic is
well documented in the source code, so
I'll comment here on the MBTOS syntax.

Line 730 uses the BAND (bit-logical-
and) function to check the byte at the
status port (INP6) for the Kaypro. If
bit 1 is 0, a complete byte hasn't been
received at the serial port. FPRINT is
used again in line 850, printing the
RESULT in a 5-character integer for-
mat with a carriage-return at the end
(the Z). Each of these seven tasks prints
a message such as "READING" beside
the screen line for that channel, letting
the user know when a particular chan-
nel is being read. Then it gets the value
from the ADC and prints that in its
proper place on the same line.
Line 880 turns on the flag K, indicating that a channel has been read, and places the channel number in the variable SENSOR. These values are used by the task that writes to the disk file.

Task 8 reads the Kaypro clock card, and again, the comments show what each line is meant to do. This task would need to be modified (maybe a little, maybe a lot) to read from a different clock.

Task 9 writes to the disk file. Since RESULT is used by all the analog-read tasks, and since the proper channel number, time, and date stamps must accompany the RESULT into the disk record, interrupts are turned off at the beginning of the task and back on at the end. Line 2740 checks to see whether a channel read has been done. In other words, if K is a 0, nothing is done. If K has been turned on by one of the other tasks, then the record is written.

The FILE 1 statement at 2750 determines which open file is used (here, we're only using one). After the FILE 1 statement, the FPRINT statement (or any regular PRINT statement) will output to the file, not the CRT screen. The FILE 0 statement that follows will cause subsequent PRINTs to go to the CRT until a file is specified again. Finally, the flag K is cleared and we're done.

Final Notes On MTBASIC
MTBASIC provides many useful features that endear it to the heart of anyone who likes to write good, readable code. Its one significant drawback, I feel, is the way redundant spaces are stripped from a program with the SAVE command. The apostrophe (single quote) can be used to define a REM statement, as you see here, but beware of the SAVE command. The compiler won't object to the next spacing that you provide to make your source readable, but if you SAVE a program from MTBASIC, those spaces will be gone!

MTBASIC (up to the version I have) does not contain any kind of editor, not even a line editor like MBASIC*. You CAN replace a line by re-typing it, and small errors can be corrected this way and the program re-compiled for testing.

To make the changes permanent in your source code, however, use WordStar*, or VDO*, or some other

Start:
Write('2)
Write('Channel to read? (0 to stop) '
Read(Channel)
If Channel = 0 then Goto Stop
Write('How many samples? (1-10000) '
Readin(J)
If J = 0 then Goto Stop
Write(J)
Gettime(Time)
Val(Time,StartSeconds,Code)
StartSeconds := StartSeconds + 0.01
Writein('Started at: ',(StartSeconds):7,2)
For I := 1 to J Do Buffer(I) := Analogin(Channel)
Gettime(Time)
Val(Time,StopSeconds,Code)
Writein('Stopped at: ',(StopSeconds):7,2)
Elapsed := StopSeconds - StartSeconds
Writein('Elapsed time: ',(Elapsed):7,2, ' Seconds')
Samples := J
Writein('Sample Rate: ',(Samples / Elapsed):7,2, ' Samples/Second' '
Writein('How many samples to display? (<RET> for all) '
Readin(J)
Writein()
For I := 2 to J Do Write(Buffer(I),'
Writein()
CalculateStandardDeviation
Writein('Sample mean: ',(SampleMean):7,2)
Writein('Standard Deviation: ',(StandardDeviation):7,2)
Writein('Press (RETURN) to continue... '
Readin())
Goto Start
Stop
End.

windowing feature, which takes time to execute. Testing an ADC-1 with nearly identical versions of HISPED, written in MBASIC, MBASIC and Turbo Pascal, I got results of 10, 88, and 103 samples per second, respectively. I recommend MTBASIC for applications that can benefit from interrupt-driven multitasking capabilities, as can many Real World computing applications.

Jerry Houston has degrees in Business Administration and Data processing, and has published numerous articles in the area of Real World computing. He is Marketing Director for Remote Measurement Systems, Inc. of Seattle and teaches computer programming at Griffin College and Seattle Central Community College. Mail will reach him at:

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One of the reasons I became so interested in computers is that they enable me to control mechanical devices such as lathes, robots, and other motion producing devices. I have a long list of mechanical projects to work on and I have most of the computer programming and interfacing information I need, but I have had difficulty locating application information on the small gears, shafts, bearings, motors, timing belts, etc. needed for assembly. We live in a rural area, and the saying is “If it doesn’t fit on a steer or a sawmill you can’t get it.” I can get information on two inch diameter shafts and bearings, but if I ask about 1/16” shafts they look at me like I’m nuts (I probably am or I wouldn’t be messing with computers, but that’s beside the point). I was very happy to locate this Data Book, and they also have Master Catalog 757 “Handbook of Small Standardized Components” in which they list parts for sale.

It is difficult to review this Handbook because it is a collection of text, charts, and specifications on a wide variety of subjects and I can’t list everything it covers. I’ll briefly describe the various sections, and comment on the parts I find especially interesting.

• Section D1 Designers Data. Part A: Mathematical formulas and tables including energy, inertia, torque, power and work. Part B: Conversion of units. Part C: Properties of materials, finishes and surface texture, including properties of steels, heat treating terms, hardness conversions, and surface texture and roughness measurement. Part D: Useful shop data, including drill sizes, standard gages, primary fits, press fits, bushing and shaft bore sizes. Part E: Fasteners including standards for inch size fasteners, screw head dimensions, strength of fasteners, screws and pins, pin standards, and non-metallic fasteners.

• Section D2 Practical Design Hints. Part A: Machining: minimizing machining, effective clamping, simplifying machining with composite assemblies, turning, boring, drilling, milling, broaching, grinding, and tapping. Part B: Assembly: avoiding over-specification of dimensions of mating parts, allowing for thermal expansion and wear, designing for accessibility and ease of disassembly.

• Section 1 Gear Design. This section can help you understand the terms you will run into when specifying and ordering gears. It covers involute curves, pitch circles, diametral pitch, gear nomenclature, pitch diameter and center distance, pressure angle, helical gears, spur gears, racks, internal gears, worm gears, bevel gearing, strength, materials, lubrication, etc.

• Section 2 Speed Reducers and Gear Trains. While the previous section covered the subject of gears, this section covers two or more gears working together (called a gear train). Some of the topics are: simple spur gear trains, compound spur gear trains, planetary gears, bevel gear differentials, force transmission, tangential forces, power flow, efficiency, minimization of gear train inertia, backlash, transmission error, configurations and designs, limit stops, etc.

• Section 3 Basic Electricity. A brief section on AC circuits, fundamental laws, and three-phase power.

• Section 4 Motors. Part A: Design considerations, motor characteristics, selecting gear motors, motor speed controls. Part B: Escap® ironless rotor DC micromotors and step motors, physical laws governing Escap motor applications, basic motor physics, gearboxes, control of DC micromotors, tutorial on micromotor application problems, Escap step motors. Part C: Step motors; principal types primary step motor performance characteristics, damping, step motor driving and control, considerations for step motor applications.

• Section 5 Negator® constant force springs and spring motors. Design, stock parts, safety considerations.

• Section 6 Couplings and Universal Joints. Types of flexible couplings, kinematics, joint selection, torque rating, secondary couples, joints in series.

• Section 7 Drives. Part A: belt and chain drives; V belt design data, roller chain design data, center distances, lubrication, tensioning mechanisms. Part B: synchronous belt drives: belt tooth configurations, pulley pitch, design and installation, standards, drive ratio tables, center distance factor tables, design based on horsepower or allowable torque.

• Section 8 Shafts and bearings. Determination of stresses for solid shafts, sizing porous-metal bearings, non-metallic bearings, ball bearings, bearing loads, tolerances and clearances.

• Section 9 Vibration and shock isolation. Fundamentals of vibration, vibration analysis, vibration mounts.

• Section 10 Introduction to robots. Manipulator geometry; joints, degrees of freedom, yaw pitch and roll, dimensions of a link, geometric configurations, load capacity, Manipulator motion, manipulator components, applications.

This data book does not cover the subjects as extensively as an Engineering manual but it would take a shelf-full of books to cover this wide range of information. It is very useful for the computerist who needs a quick source of information on sizes, fits, gears, center distances, strength, and other information.
Unsoldering

(continued from page 18)

technicians who are intimately familiar with the product. Confronted with a bad board they often zero in on the offending chip with just a few simple tests. Rarely do they need to unsolder more than one or two IC’s before they cure the malfunction. Anyone who has repaired many boards can confirm this fact because a few, very enlightened vendors will include the troubleshooting steps in the documentation and by following that data you can quickly isolate the bad components. Otherwise if unfamiliar with a circuit you may wind up unsoldering five, six, seven or more IC’s before achieving a fix. Clearly if the IC’s are in sockets the repair process will proceed far quicker and easier.

Printed Circuit Boards,
The Inside Story!

It may seem out of place to talk about circuit boards in this article when the next will be all about repairing them, but since unsoldering failures are the chief cause for needing to repair them, it’s useful to briefly discuss them so as to limit the potential problems. Actually we’re going to talk about geography and social issues that may seem to have nothing do with our prime subject but in fact they do.

In North America most circuit boards are manufactured not by the kit makers or product assemblers, but by specialized firms that act as subcontractors. Many of the boards these firms produce are destined for products that will be used by the Military. When circuit boards first appeared the Military tested them under the harsh conditions their equipment must endure, and the early boards failed miserably, however the potential benefits of boards led makers to develop types that could stand up to Military testing. Eventually these types were approved and a set of specifications called ‘Mil Specs’ defines how these boards are to be made. Typically these boards use glass filled epoxy construction techniques which require special manufacturing procedures but the resulting boards are very durable, indeed. Most circuit board makers must be able to produce Mil Spec boards and find it economical to only produce that type, which means that even if a company doesn’t need such rugged boards they will get them anyway and it won’t cost extra.

If a circuit board was made in North America then chances are it is of epoxy type construction and can withstand unsoldering heat very well, also the traces will adhere a little better thus reducing the chance of delamination. These boards often seem able to withstand an M-1 Tank.

In the Far East which is the other great source of products with circuit boards there is almost no need to meet Mil Specs and most product makers produce their own boards. They make boards the old fashioned way which is a paper laminating technique. These boards stand up well to consumer use but not so well to unsoldering heat. It’s quite easy to delaminate both the traces and even the board itself by using too heavy a hand with the iron.

What we said above is only a generalization, both types of boards are produced in both places so knowing the origin of a board is just the first step in identifying its construction. Epoxy boards are usually a light beige color in their raw state but more likely are green or blue due to an applied solder mask. Paper laminate boards are often
With these tools you can really program!

S-C Macro Assembler — Version 2.0 is the latest in a long line of enhancements to the oldest commercially available assembler for Apple computers. Supports the full instruction set and all addressing modes of the 6502, 65C02, 65802, and 65816 processors, as well as Steve Woznak's SWEET-16 pseudo-processor. Compatible with any Apple II, II Plus, //e, or //c having at least 64K RAM and one disk drive.

S-C Macro Assembler is well-known for ease-of-use and powerful features, rated number one by "The Book of Apple Software". You get an integrated editor/asmblery with over 29 commands and over 20 directives including macros, conditional assembly, global search/replace, edit, and move. Thousands of users in over 30 countries and in every type of industry attest to its speed, dependability, and user-friendliness. S-C Macro Assembler blends power, simplicity, and performance to provide the optimum capabilities for both beginning and professional programmers.

Both DOS 3.3 and ProDOS versions are available now. $100 each, you can save by getting both together for only $120. Upgrades are available for owners of previous versions. Disks are not copy-protected, so you can easily make back-up copies.

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2331 Gus Thomasson, Suite 125
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(214) 324-2050

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5. Remember nothing works all the time and everything can be fixed, look for the next article in this series.

Sources
To say that we’ve only scratched the surface of the art of unsoldering is to put it mildly. Clearly the key to reliably practicing this art is to practice it. But not on the motherboard of a two thousand dollar computer, use a scrap board. Where can you get scrap boards? Check with the local TV or Stereo repair place, they may be throwing them away. A local electronics manufacturer may also be a source, be sure to ask if you can take any apparently discarded boards. Some surplus electronics outlets sell scrap boards, people buy them to experiment with or to salvage parts off them. Remember that just like functioning boards, scrap boards come in different types and present a variety of unsoldering challenges.

Look in the back of electronics magazines for the names of surplus firms. Below is a firm that currently has a limited supply of boards with relays and other parts that are sold as salvageable.

John J. Meshna, Jr. Inc., PO Box 62, E. Lynn, MA 01904 (617)595-2275. Cat. No. SP-88A has one relay and lots of parts for $2.00, SP-88/2 two relays and parts, $2.00, SP-88/3 three relays for $3.00 etc. They do have a $15 minimum order so you may wish to get their catalog No. SP-36 and see all the weird stuff they sell.

For a good catalog of unsoldering tools: Edie Electronics, 2700 Hempstead Turnpike, Levittown, NY 11756, (516) 645-4722.

A short-form catalog of soldering and desoldering tools is available by writing to: Customer Service, OK Industries, 3455 Conner St., Bronx, NY 10475.
Build the Circuit Designer 1 MPB
Part 2: Programming the Single Board Computer
by Neil Bungard

Introduction
In the last issue of The Computer Journal we looked at Part 1 of an article describing the Circuit Designer 1, MPB (CD-1, MPB). In that article, the design of the CD-1, MPB (an 8035 based single board computer) was presented and its operation was explained. This month I will mention a few applications for which I have personally used the CD-1, MPB, and we will present a simple read/write memory circuit which plugs into the CD-1, MPB program memory socket and allows the user to enter programs in machine language.

The CD-1, MPB was originally created because of the need for a simple, general purpose, single board computer to be used as an intelligent control unit in several projects which were under construction. Among these projects were an automatic telephone number recording system, a CO gas monitoring instrument, and a classroom microprocessor trainer. The CD-1, MPB successfully served as the control unit for all of these projects with changes only in the software from project to project.

Programming the CD-1, MPB
The Instruction Set. As in any microprocessor control system, operations are controlled via program instructions. The 8035 has an instruction set which consists of 90 commands. The 8035 literature is out of print, but specifications on the 80C35 which has the same instruction set are available from Intel by calling 1-800-538-1876 (or 1-800-672-1833 in California) and requesting the 80C48/80C35 specifications #210943-001. The instruction set for the 80C48, 80C35, 80C49, and 80C99 is shown in Table 4.

The Interface Sockets. The CD-1, MPB communicates with the outside world via its on board interface sockets. The placement, pin assignment, and explanation of the interface sockets are shown in Figure 12.

Using R/W Memory In The Program Memory Space. Since the 8035 can only execute instructions from its program memory, which is read only memory, there is no capability for writing dynamic program instructions into the program memory area. Furthermore, since the size of the program memory is only 4K bytes, the manufacturer decided not to give the CPU direct memory access (DMA) capabilities. What this means is that the program memory of the 8035 was meant to be PROM or EPROM.

With the CD-1 memory board it is possible to write dynamic programs into the 8035 program memory space. This is done by having the CD-1 memory board perform a DMA. The DMA is accomplished by the following sequence of events:

1. (Refer to Figure 15 concerning this explanation). Placing the run/examine switch on the memory board to the examine position (closed) pulls the reset input on the CPU LO through diode D1. (This puts the CPU in the reset mode).
2. The data bus is disconnected (Tri-state) from the CPU when the CPU is in the reset mode. Also with the run/examine switch in the examine position a ground is applied to the base of inverting transistor T1. The output of the inverting transistor is applied through the memory board interface socket and disconnects the address bus multiplexer outputs of the CPU board from the memory ICs (IC6 and IC7). The address and data buses are now free for use by the memory board.
3. The address and data lines to the memory ICs are connected to a set of DIP switches via IC1, IC2, and IC3 (74LS137's). When the run/examine switch (S1) is in the examine position (closed) a ground is applied to pin 1 of IC1 and IC2 thus connecting one side of the address DIP switches to the address inputs of the memory ICs. The memory ICs are enabled via a ground applied by switch S1 through diode D2. The data DIP switches remain disconnected from the memory ICs until the deposit switch (S2) is depressed, and logic states are applied to the address and data bits via the DIP switches. When the switches are open the lines are floating and the memory ICs see a logic 1. To apply a logic 0

Figure 10: Photo of the completed CD-1.
to an address, or data bit, the bit must be grounded by closing its corresponding DIP switch.

4. Examining the contents of the memory locations is accomplished with S1 in the examine position (closed) and S2 not depressed (open). In this configuration the memory ICs will be in the read mode and the contents of the memory location set on the address DIP switches will be reflected on the memory board displays. Programming is accomplished by setting the address and data DIP switches to the desired values and depressing the deposit switch (S2). This writes the data into the selected memory location, which is accomplished by simultaneously enabling pin 1 of IC3 (74LS373), and pulling pin 10 LO on the memory ICs. When the deposit switch is released the charge will be reflected by the memory board displays.

5. After all the instructions have been entered into memory the program is executed by placing the run/examine switch in the run position (open). This disconnects the memory board data and address lines from the program memory, connects the CPU address and data lines to the program memory, and removes the ground from the reset pin on the CPU. This is accomplished in exactly the reverse order as going from the run mode to the examine mode in the explanation above.

PSEN is the program memory read pulse which inputs instructions to the CPU. PSEN enters the memory board via pin 24 of the interface socket and goes to the CS input of the memory ICs. This pulse is active LO. Also, a capacitor and resistor circuit is connected to the reset pin on the CPU to cause a small time delay when going from the examine mode to the run mode. This is necessary to ensure that the address bus multiplexer outputs from the CPU are completely enabled before the CPU begins executing instructions.

In the CD-1 Program Memory Board design (see Figure 15), 2114 memory ICs were used. These ICs are 1K by 4-bit memories, and by utilizing two 2114's, 1K of program memory was obtained. If you need more than 1K of program memory, you can use different memory ICs or add additional 2114's and decode the higher order address bits to enable the appropriate 1K "block". The 8085 has the capability of accessing up to 4K bytes of program memory.

Once a program has been written and debugged using the CD-1 program memory board, the same program can be written into a 2716, 5 volt EPROM which will directly replace the memory board. The 2716 EPROM is non-volatile memory which means that when power is removed it will retain its program. This enables the CD-1 microprocessor board to be utilized as a dedicated monitor and/or control device. The CD-1 microprocessor board has sockets to accommodate two 2716's so that frequently used subroutines could be written into a 2716 and placed in the higher 2K byte memory socket. With the CD-1 memory board plugged into the lower 2K byte memory socket, dynamic programs can be written with the ability to call subroutines from the 2716 EPROM on the microprocessor.
This is a diagram of the pin assignment and placement for the interface sockets.

- **A0 through A11-multiplexed address bus outputs.**
- **D0 through D7-data bus interface (bidirectional).**
- **P10 through P17-I/O port 1 interface (unidirectional).**
- **INT (0 through 7)-priority interrupt request inputs (active low).**
- **IACK (0 through 7)-interrupt acknowledge outputs (active low).**
- **AMOD-address multiplexer output disable (active high).**
- **RESET-CPU reset (active low).**
- **RD-external data memory read pulse.**
- **WR-external data memory write pulse.**
- **TO-a test input which may be sampled by a conditional jump instruction. The internal CPU clock signal can be output via TO.**
- **T1-A test input which can be sampled by a jump-on-condition instruction. T1 can also be used to input a signal to counter/timer logic when it is serving as an event counter.**
- **MD-External memory disable.**
- **OUT (0 through 7)-output device code pulses.**
- **IN (0 through 7)-input device code pulses.**

**Figure 12:** Interface sockets.

**Construction of CD-1, MPB**

Double sided circuit boards have presented a problem to hobbyists in the past because of the need for plated through holes. The CD-1 utilizes a double sided board but to eliminate the need for plated through holes I have inserted and soldered wires on both sides of the feed through holes, soldered passive components (resistors, capacitors, etc.) on both sides of the board, and used Molex® connectors (soldered on both sides) for the ICs. Care must be taken to insure that cold solder connections are avoided. One faulty connection on a single board computer could cause many hours of troubleshooting misery before the problem is located. A parts placement layout is provided in Figure 13 to aid in the construction and assembly of the CD-1, MPB, and a complete parts list is provided in Figure 14.

Figure 10 is a photograph of the completed CD-1, MPB. Full size positive prints of a silk screen outline and the front and back foil patterns are available for $1.00 postage. Film
### Table 4: 80C85/80C48 Instruction Set  
**Courtesy of Intel.**

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADD A, R</td>
<td>Add register to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ADD A, #R</td>
<td>Add data memory to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ADD A, #data</td>
<td>Add immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>ADDC A, R</td>
<td>Add register with carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ADDC A, #R</td>
<td>Add data memory with carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ADDC A, #data</td>
<td>Add immediate with carry</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>ANL A, R</td>
<td>And register to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ANL A, #R</td>
<td>And data memory to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ANL A, #data</td>
<td>And immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>ORL A, R</td>
<td>Or register to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ORL A, #R</td>
<td>Or data memory to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ORL A, #data</td>
<td>Or immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>XRL A, R</td>
<td>Exclusive or register to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>XRL A, #R</td>
<td>Exclusive or data memory to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>XRL A, #data</td>
<td>Exclusive or immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>INC A</td>
<td>Increment A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>DEC A</td>
<td>Decrement A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CLR A</td>
<td>Clear A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CPL A</td>
<td>Complement A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>DA A</td>
<td>Decremental adjust A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>SWAP A</td>
<td>Swap nibbles of A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>RLA</td>
<td>Rotate A left</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>RLC A</td>
<td>Rotate A left through carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>RRA</td>
<td>Rotate A right</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>RRC A</td>
<td>Rotate A right through carry</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Data Moves

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>MOV A, R</td>
<td>Move register to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOV A, #R</td>
<td>Move data memory to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOV A, #data</td>
<td>Move immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>MOV R, A</td>
<td>Move A to register</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOV R, #</td>
<td>Move A to data memory</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOV R, #data</td>
<td>Move immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>MOV P, A</td>
<td>Move PSW to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOV P, #</td>
<td>Move PSW to data memory</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOVX A, R</td>
<td>Move register to data memory</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOVX A, #R</td>
<td>Move data memory to A</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOVX A, #data</td>
<td>Move immediate to A</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>MOVX P, A</td>
<td>Move register to data memory</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOVX P, #</td>
<td>Move register to data memory</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOVPS</td>
<td>Move to A from accumulator</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Timer/Counter

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>MOV A, T</td>
<td>Read timer/counter</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>MOV T, A</td>
<td>Load timer/counter</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>STR T</td>
<td>Start timer</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>STRT CN</td>
<td>Stop timer</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>EN TCON</td>
<td>Enable timer/counter interrupt</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>DI TCON</td>
<td>Disable timer/counter interrupt</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Control

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>EN I</td>
<td>Enable external interrupt</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>DL</td>
<td>Select low operation</td>
<td>1</td>
<td>1</td>
</tr>
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</table>

### Branch

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>JMP addr</td>
<td>Jump unconditional</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JMPR, A</td>
<td>Jump indirect</td>
<td>1</td>
<td>2</td>
</tr>
<tr>
<td>DUNZ, R</td>
<td>Decrement register</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JNC addr</td>
<td>Jump on carry = 0</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JZ addr</td>
<td>Jump on zero</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JNZ addr</td>
<td>Jump on not zero</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JSE addr</td>
<td>Jump on sign extension</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JLE addr</td>
<td>Jump on less than or equal to</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JGT addr</td>
<td>Jump on greater than</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JLE addr</td>
<td>Jump on less than or equal to</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JAE addr</td>
<td>Jump on equal or greater than</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JBE addr</td>
<td>Jump on equal or less than</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JNAE addr</td>
<td>Jump on not equal or greater than</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>JNBE addr</td>
<td>Jump on not equal or less than</td>
<td>2</td>
<td>2</td>
</tr>
</tbody>
</table>

### Subroutine

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>CALL addr</td>
<td>Jump to subroutine</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>RET</td>
<td>Return</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>RETR</td>
<td>Return and restore status</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Flags

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>CLC</td>
<td>Clear carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CPL</td>
<td>Complement carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CLF</td>
<td>Clear flag</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CPLF</td>
<td>Complement flag</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

---

C1 — 10 mF electrolytic capacitor.  
C2 — 22 mF tantalum capacitor.  
C3 through C9 — 0.1 mF ceramic disc capacitor.  
D1 — 1 amp silicon diode.  
D2, D3 — 1N914 switching diode.  
IC1 — 8035 CPU.  
IC2, IC3 — 74LS273 octal tristate latch.  
IC4, IC5 — 27162 2K x 8 read only memory.  
IC8 — 74LS280 dual 5-input positive NOR gates.  
IC7, IC8, IC18 — 74LS138 3-to-8 line decoders.  
IC9 — 74LS82 quad 2-input positive OR gates.  
IC10, IC11 — 2112 random access memories.  
IC12 — 74LS86 quad 2-input exclusive or gates.  
IC14 — 74LS00 quad 2-input positive NAND gates.  
IC15 — 74LS74 dual D-type positive-edge-triggered flip flops.  
IC16 — 74148 8-line-to-3-line octal priority encoders.  
IC17 — 74LS30 8-input positive NAND gate.  
LED1 — 0.200 red light emitting diode.  
R1 through R5 — 1000 ohms.  
R6, R7 — 4700 ohms.  
R7 — 330 ohms.  
S1, S3 — SPST switch.  
S2 — SPDT momentary switch.  
XTAL — 1MHz to 6 MHz crystal.  
MISC — (6) 14 pin low profile IC sockets. (10) 18 pin low profile IC Sockets. (1) 4 pin low profile IC socket. (2) 28 pin low profile IC sockets, printed circuit board, wire, aligsten clips, rubber feet, hardware, 5 VDC supply @ 1 amp, solder, etc.

**Figure 14:** Parts list for CD-1 MPB.

### Registers

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>INC R</td>
<td>Increment register</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>INC @R</td>
<td>Increment data memory</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>DEC R</td>
<td>Decrement register</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Control

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>EN I</td>
<td>Enable external interrupt</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>DI TCON</td>
<td>Disable external interrupt</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>SEL RB0</td>
<td>Select register bank 0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>SEL RB1</td>
<td>Select register bank 1</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>SEL MB0</td>
<td>Select memory bank 0</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>SEL MB1</td>
<td>Select memory bank 1</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>ENCLK</td>
<td>Enable clock output on F0</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Subroutine

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>CALL</td>
<td>Jump to subroutine</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>RET</td>
<td>Return</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>RETR</td>
<td>Return and restore status</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

### Flag

<table>
<thead>
<tr>
<th>Mnemonic</th>
<th>Description</th>
<th>Bytes</th>
<th>Cycles</th>
</tr>
</thead>
<tbody>
<tr>
<td>CLC</td>
<td>Clear carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CPL</td>
<td>Complement carry</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CLF</td>
<td>Clear flag</td>
<td>1</td>
<td>1</td>
</tr>
<tr>
<td>CPLF</td>
<td>Complement flag</td>
<td>1</td>
<td>1</td>
</tr>
</tbody>
</table>

**Note:** This list may be extended and may be subject to change.  
**Conclusion:** As mentioned earlier, there may be times when you will want to write larger programs to be executed by the CD-1 MPB. Furthermore, you may want to purchase an 8035 assembler so that you can write the code in assembly instead of machine language. In the next issue of *The Computer Journal* I will present a device which will make these things possible.  
(Continued on page 47)
New Products

ROMable MTBASIC Now Available

Softaid, Inc. has released a new version of MTBASIC for OEMs and manufacturers of board level computer products. The new version, MTBASIC*ROM, can be burned into ROM and will execute from ROM. It is only available for Z-80 systems.

MTBASIC*ROM has all of the features of normal MTBASIC except for disk commands, since ROM based systems generally don’t have a disk connected. Without the disk commands, MTBASIC*ROM will fit into 24K of ROM. A special version is available with disk commands, but it requires 30K of ROM.

Since every vendor’s system has a different configuration, and CP/M® is usually not resident, MTBASIC*ROM is supplied on disk as .REL and .LIB files. A single assembly language module must be provided by the vendor to configure the I/O for the target system.

There are no royalties for distributing MTBASIC*ROM in a board level product. MTBASIC*ROM is available for $1,100 from Softaid, Inc., PO Box 2412, Columbia, MD 21045, phone (301) 792-8096.

Linear Optimizer

The Acme Computer Company has announced release 2 of their Linear Optimizer program. Among the enhancements in the latest release are:

- Subscribed variables with up to 127 dimensions. Means for declaring array constants together with array operations and conditional constructs make defining large problems quick and easy.
- Input and output statements. Several file formats are supported, including the ubiquitous DIF format already used by dozens of commercial packages. Automatic dimensioning (“input until end of data”) together with the conditional forms mentioned above make possible size independent matrix generators - perfect for imbedded applications.
- Report statements, which allow the user to include values in the final report that can be computed from the solution. This not only eliminates the need for additional constraints, it allows the display of nonlinear functions of the solution values.

Linear Optimizer with its high level matrix generator language makes it possible to create special applications that require no knowledge of linear programming by the user. Like spreadsheet templates and data base applications, Linear Optimizer models add the essential elements to a useful, powerful package: programmability and integration.

Linear Optimizer is available for PC-DOS 2.0®, PC-DOS 3.0®, or CP/M® systems for $400. To order, or to request more information, contact Stephen Vestal at The Acme Computer Company, Box 51193, Seattle, WA 98115 phone (206) 522-8655.

Programmable Microcontroller

Basicon now offers a 3 x 4" fully programmable microcontroller with 2K RAM and 40 I/O lines for less than $200.

With INTEGER BASIC as its resident programming language, the MC-1Z is a versatile controller for a wide range of applications, including OEM instrumentation, process control, research and development and even personal computing. It is based on the Zilog 8671 processor and can be upgraded to 16K RAM on the same board or with a 4K or 8K EPROM.

The MC-1Z is fully self-contained, easily programmed and comes with a variety of peripherals designed to maximize its utility. Included is a clock/calendar, two fast timer/counters, 6 interrupts and EPROM receptacle. Add cabling and an RS-232-C compatible terminal and you have a complete applications programmer with baud rates from 110 to 19.200. Complete hardware and software manuals are included, and the MC-1Z is available for shipment.

Contact Fred King at Basicon, Inc., 11895 NW Cornell Road, Portland, OR 97229 phone (503) 826-1012.

Higher Sampling Speed for ADC-1

Remote Measurement has announced an improved version of their ADC-1 Data Acquisition and Control System. The analog channel sampling rates have been increased for a choice of 7 to 15 samples per second to a choice of 20, 50 or 100 samples per second.

Application notes are available that provide users with Turbo Pascal® routines that can be used to acquire data at the speeds the ADC-1 is now capable of. They point out that BASIC programs can be still used, and even the BASIC programs will show an increase in speed with the new version.

The ADC-1 is compatible with any computer with an RS-232-C serial interface. The system provides sixteen analog input channels with a resolution of 12 bits plus sign (11 bits at high speed), four digital inputs, six hardware controlled outputs, and the ability to operate SSR-type AC line-carrier remote control modules. The ADC-1 allows the user to concentrate on the design of the project and program, free from the details of electronic interfacing.

The host computer or modem sends commands to the ADC-1 in a single 8 bit byte, through a serial port. For each byte received, the ADC-1 returns an 8 byte packet that contains status and measurement data. The communications interface supports standard RS-232-C as well as the TTL levels used by Commodore and Atari serial ports. Baud rates are selectable from 300 to 9600 bps. Interconnection instructions and programming examples for most popular microcomputers are included in the owner’s manual.

Sensitivity of the analog inputs is 0.1 mV offer a range of ±0.4 V (modifiable to ±4.0 V). The A/D inputs are user-configurable for operation at 20, 50, or 100 samples per second. An optional instrumentation amplifier provides sensitivity at microvolt levels for use with thermocouples. The four digital inputs may be used to monitor or count on/off signals at up to 400 Hz, while the six controlled outputs can operate external electronic devices. Remote control of lamps and appliances is an additional capability of the ADC-1, via signals
transmitted over AC wiring to line-carrier remote control modules of the BSR X-10 system.


**Universal MAC INKER**

Computer Friends has announced the new Universal Cartridge Ribbon Re-Inker. Now, one Mac Inker re-inks just about any fabric ribbon cartridge available currently or in the future.

They claim that operation is extremely simple and wholly automatic, and that their new formula, lubricated, carbonless ink yields a printout darker than any original ribbons. They further state that a good quality ribbon can be re-inked up to 80-100 times, depending on its original length.

The Mac Inker can also be used to ink (and then re-ink) blank cartridges in any of 6 brilliant colors (or combinations thereof: Red, Blue, Green, Brown, Yellow, or Purple using the new Computer Friends ink.

The cost is $60 for the universal base and $8.50 for each cartridge driver kit, from Computer Friends, 6415 SW Canyon Ct., Suite 10, Portland, OR 97225, phone (503) 297-3231 or (800) 547-3303.

**Editor’s Note:** We have been using a MacInker for over a year to ink six to eight Epson MX-80 ribbons a month, and are would not want to do without it.

**Basic-52 Computer/Controller**

The Micronaut BASIC-52 Computer/Controller is a standalone single board microcomputer which needs only a power supply and terminal to become a complete system programmable in BASIC or machine language. The BASIC-52 uses the Intel 8052AH-BASIC microprocessor which contains a ROM resident 8K byte floating point BASIC interpreter. It contains sockets for up to 48K bytes of RAM/EPROM, an “intelligent” 2764/128 EPROM programmer, 3 parallel ports, a serial terminal port with autobaud rate selection, a serial printer port, and is bus compatible with the Micronaut BCC11/BCC21 Z8 Systems/Controllers and BCC series expansion boards.

The BASIC-52 treats the EPROM as “write once” mass storage. When a BASIC application program is saved to EPROM, it is tagged with an identifying number and stored only in the amount of EPROM required to fit the program. Additional application programs can be stored to the same EPROM and recalled for execution by requesting a particular ROM number or designated as an autostart program.

Since the BASIC-52 is bus oriented, it supports Micronaut’s Term-mite ST smart terminal board, BCC14 memory expansion board, BCC33 I/O expansion board, BCC40 AC/DC I/O board, BCC13 A/D converter board, and BCC08 serial I/O expansion board.

The BASIC-52 Computer/Controller with 8K bytes of RAM is available for $239. For orders call 1-800-635-3355. For technical information or to request a data sheet call 1-203-871-6170.

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**(Continued on page 50)**
changes until it works. They also design languages which require many steps and considerable time between the writing of the code and execution of the program. While this approach may be acceptable for business related applications where the desired result is known in advance, it is not suitable for experimental work such as robotics or control where the program is a tool and not the final result.

What I would like is a programming tool (should it be called a language?) that is easy to program, generates small ROMable fast acting code, is interactive, and can be customized or extended by the user. I'm looking at this from the point of a user who has to program in order to accomplish their goal in a fast changing experimental situation instead of a programmer who is writing code for a canned program to be used by someone else. I haven't had the time to fully define what I want, but I'll list some initial thoughts hoping that you'll add your input. Perhaps we can generate enough interest to start a special user's group to work on this project.

I would like to be able to write sections in assembly language, add them to a library, and call only the required routines from a higher level language which I'll refer to as the "Handler". This sounds like a Macro Assembler, but I was thinking of the Handler as a high level language with built-in routines for the non-critical code sections. A possible candidate is C, and I have the BD Software C compiler to evaluate for this use. BDS C has the advantages of a small run time overhead, the ability to include only the library files (which can be either C or 8080 assembler HEX code) actually used, and it can produce ROMable code. The awkward part is that you have to use a word processor to generate (or change) the source code, then compile and link the program before you can run it. Then if you want to change a portion written in assembler, you have to rewrite the source using a word processor, assemble to a HEX file, put the new file in the library, and then compile and link the C program. I'm putting heavy emphasis on the ease of revising the program, and many people won't agree with this because they don't need it, but when working on a robotics application you'll want to make frequent changes in the sequence of motions, the speed, the position, the dwell time, etc. In these cases, an interactive BASIC-like language would be convenient if it was fast enough for real time control.

Another possibility for the Handler is MTBASIC which includes a flash compiler, has the ability to call machine language subroutines with argument passing, and can include in-line machine language code. You can even write a subroutine with CODE statements and then access it with a GOSUB.

The most likely of the current languages for the Handler is Forth with a special dictionary of machine language subroutines for the speed critical routines, but I can't seem to get started in Forth. The concept of the language is easy to understand, my problem is taking the time to learn to use the nonconventional editing, file saving, and user interface. It would help if there was a local Forth advocate who could take the time to get me started. Perhaps one of our readers would like to write an article on getting started with Forth — not a description of the language, but the mechanics of doing something with it.

---

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Published every couple of months, and consecutive issues are completely different. Send $1.00 for next three issues.

JERRYCO, INC. 601 Linden Place, Evanston, Illinois 60202
One of the new developments is 'Structured Assembly Language Programming for the Z-80' from Hayden. Their approach is to use a large library (which they supply on disk) of macros for commonly used routines, and then write your specific routines in assembly. We have a copy of this for review, and will have more to say about it later.

Turbo Pascal

As you can tell from reading this, I've been doing a lot of thinking about languages. I've haven't had time to do much programming, but I have been thinking about it while doing other things. When I first saw Pascal I decided that it had nothing to offer and I completely ignored it, but I recently tried Turbo Pascal from Borland and it changed my mind about Pascal. There has been a lot written about Turbo Pascal so I won't re-hash it, but you should be aware of its editor with WordStar-like commands, ability to run programs from memory, compile directly to disk in one step, and a very helpful error detection and correction system.

Jerry Houston's article in this issue is the first of a series on Turbo Pascal, and we are interested in additional articles from other authors. We'd like to hear from you if you have an article in mind, and we'd also like to publish short notes, letters, information on public domain software, tips, routines, or questions on Turbo Pascal.

CP/M is Dead, Long Live Z

We have been told that Digital Research has classified their products A-D according to how much support the products will receive with D being the category with the least support. Concurrent CP/M is in group D and CP/M Plus is in group C, which means that in effect there is no support! Apparently they have also discontinued selling individual copies of uninstalled CP/M, and will only sell to hardware OEMs who will ship installed versions with their hardware.

How can you obtain an operating system for a Z-80 or HD64180 single board computer you design? You either pirate CP/M and end up with an illegal unsupported system, or you find another supplier. A better choice than CP/M (even if it was available) is the Z-System from Echelon, because it does so much more than CP/M, and it is supported by people who care about YOU the user. It is time to change to an improved, up-to-date, supported system even if you already have CP/M up and running. Write to Frank at Echelon, 101 First Street, Suite 427, Los Altos, CA 94020 for information on the Z-System. And if you implement it for a new application, share the BIOS with other readers.

We Are The Pioneers of the Future

What will computers be like five years from now? Predicting the future for computers is very difficult. If you doubt it, check some of the forecasts made five years ago and see how off-base they were. I'll stick my neck out and make a forecast for the state of microcomputers in the year 1991 (I hope that you'll still be reading TCJ then). My prediction is that in the year 1991 microcomputers will be nothing like the ones we have today, and we have no basis on which to forecast what they will be because the technical features they will use are unknown (or at least not well known) and would not be feasible with current technology.

I realize that predicting that we can not make a prediction is a cop-out, so I'll try to forecast some trends or needs that should be met. One of the most obvious needs is for standardized easy to use hardware, operating systems, and software. Any reasonably intelligent person—someone who can use a telephone, a stero, or drive a car—should be able to use a computer
with a few minutes introduction. And they should be able to use any other computer from a different manufacturer without relearning anything. The development of the automobile is a good example of what will have to be done by the computer industry. In the beginning there were many companies making dissimilar products, but the market forced them to produce cars with standardized controls. Today we can get into a Ford, Chevy, Honda, Dodge, or Volkswagen, and travel to our destination over the same roads, using the same maps, stopping at the same gas stations, without having to be reeducated about how to operate the car. There are non-standard high performance vehicles which require special skills, but they are for the specialist and not the general public. My first prediction is that the unsophisticated user will be able to put their I know that user is singular and their has been plural, but I refuse to use he/she or his/her so I am using their instead program into any one of a large number of computers and run it using the same controls—they'll hardly know that they are using a different computer.

Floppy disks are slow and easily damaged. Hard disks have more storage capacity and are faster, but they are not portable and are prone to catastrophic crashes. Some form of disk may be suitable for computer centers, but the normal user needs something which is non-mechanical, not easily damaged, fast, has very large capacity, and can be taken to another computer across the street or across the country. Optical write once disks sound like a temporary solution because they have large capacity and preserve the data because you do not write over the files, but I feel that the mechanical complexity and potential problems rule them out as a long term solution. My second prediction is that high capacity solid state non-volatile memory which does not require batteries will be in general use. Plug in cartridges will be used for program distribution, data backup, and data interchange, but the computer will have enough non-volatile memory to hold all the programs and data used by the average person so that they load the computer once and then just turn it on and use it without having to reload.

One of the current buzz-words is "user friendly", but programs are NOT easily used by the general public and a lot of work still remains to be done in this area. One of the problems is that people have been over-sold on what a computer can do. Small business owners who can not even balance their checkbook have been led to believe that if they buy a computer and an accounting program they can take care of their books. Accounting programs are a tool which enable someone capable of doing accounting to do it more easily and more accurately, but they don't enable someone with no training to set up and maintain their books. The disappointment caused by these unrealistic expectations (which are due to misleading advertising) are the primary reason for the current anti-computer backlash. It would be like me buying a scalpel today and expecting to able to perform brain surgery tomorrow just because I bought the same tool that a trained surgeon uses. My third prediction is that programs will be able to be used by their intended audience without any special computer training.

What do YOU Predict?
We are the pioneers of the future! The ideas for advancements will come from individuals who are not satisfied with what is available—people who can see a better way and figure out some way to accomplish it. I challenge you to send your predictions for publication in TCJ.

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Where these terms (and others) are used in The Computer Journal they are acknowledged to be the property of the respective companies even if not specifically mentioned in each occurrence.
Circuit Designer

(Continued from page 48)

Memory Board Parts List

D1, D2, D3 — 1N914 switching diode.
DIP 1, DIP 2 — 8 Position dip switch.
DIP3, DIP 2 — MAN 704 common
 cathode display.
IC1, IC2, IC5 — 74LS373 octal tristate
 latch.
IC4, IC5 — F9868 hexadecimal decoder.
IC6, IC7 — 21141K by 4 random access
 memory.
R1, R2, R3 — 1K 1/4 W resistor.
R4 — 4.7K 1/4 W resistor.
S1, S2 — SPST switch.
SOC 1 — 28 pin I.C. socket.
T1 — 2n3904 silicon transistor.

Figure 16: Parts list for Memory Board.

Possible. The device which I am
referring to is a 2716 EPROM emulator.
The 2716 EPROM emulator is actually
a read/write memory (with a 2716
EPROM pinout) connected to the serial
port of any personal computer through
a standard UART. In addition to
programming the CD-1, MBP, you will
find that the EPROM emulator has
many other uses.

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MasterFORTH is a state-of-the-art imple-
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way. Forth is fast too, and you can use its
built-in macro assembler to make it even
faster. MasterFORTH's relocatable utilities,
transient definitions, and headerless code
let you pack a lot more program into your memory. The
resident debugger lets you decompile, breakpoint, and
trace your way through most
programming problems. A string
package, file interface, and
full screen editor are all standard features.

MasterFORTH exactly matches the Forth-83 Stan-
dard dialect described in Mastering Forth by Anderson
and Tracy (Brady, 1984). The standard package in-
cludes the book and over 100 pages of supplemen-
tary documentation.
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- Build a High-Resolution S-100 Graphics Board, Part Three: Construction
- System Integration, Part Three: CP/M 3.0
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- Build a Code Photoreader

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- An Affordable Graphics Tablet for the Apple II
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- The Hacker's MAC: A letter from Lee Feiseinstein
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- BASE: Part Six
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S-100 8080E CPU BOARD. Detailed description in issue 16 of The Computer Journal. A & T $250, Kit $210, Bare Board $85. Prices include shipping. INTELCOMP, INC., 292 Lambourne Ave., Richfield, OH 44229, Phone (216) 646-2016 after 6 p.m.

Morrow Decision 1 S-100 system with MPZ-80 CPU, DJ/DMA floppy disk controller, 256K static ram, Wonderbus I/O on mother board, Disk Jockey Hard Disk (HDC/A) Controller, Sugar 800 floppy drive, 40MB hard disk, CP/M, Micronix Multiuser system, unconfigured MP/MII, DBase II, Wordstar, Accounting Plus. Excellent condition. $3500, some trades considered. TJC, 190 Sullivan Crossroad, Columbia Falls, MT 59912 Phone (406) 927-9119.

S-100 Bus IEEE-488 Interface Card with cable, manuals, and software for North Star Horizon. Purchased new from Pickles and Trout in 1979 and used once. $100. Call Phil Welts at (406) 755-1323 days or (406) 844-3240 evenings.


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Book Sale—These books are offered at this price while the supply lasts. Zilog Z80 CPU Technical Manual, $1.50. CRABIC User's Guide by Osborne, Eubanks, and McNiff, $14.00. Introduction to FORTH by Ken Knecht, $9.00. FORTH Programming by Leo J. Scanlon, $13.00. These prices are posted in the U.S. only. TJC, 190 Sullivan Crossroad, Columbia Falls, MT 59912.

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New Products
(Continued from page 43)

HD64180 Communications Software

Echelon, Inc. announces further support for Hitachi's HD64180 highintegration 8-bit microprocessor chip with release of TERM III, next generation communications software package which offers the following features:

- Interactive communications with remote computer systems.
- Reliable file transfer between the user's computer (host) and a remote computer.
- Control of an auto-dial/auto-answer modem.
- Access control for remote system applications (the user's system can be set up to be dialed into by outside users).
- Rapid and easy reconfiguration of the communications environment for different applications.

TERM III was designed to meet the communications needs of the serious Z-System user, and knowledge of that operating system is required to make effective use of the combination. The program works with ZCPR3 and Z-System and runs on Z-80, NSC800 and HD64180 based microcomputers. Three books, ZCPR3: The Manual, ZCPR3 and IOPs, and ZR DOS Programmer's Manual, provide detail descriptions of all Z-System functions.

TERM III is priced at $99, and is available with the other Z-System items from Echelon, Inc., 101 First Street, Los Altos, CA 94022; phone (415) 948-3820.

(Continued on page 51)

Technical Engineering BASIC

TransEra has announced TBASIC, a new technical BASIC for CAD, scientific, and engineering applications with emphasis on graphics and instrument control. They state that it is the only language for PC's with built-in GPIB syntax for both graphic and instrument type peripherals. It permits easy adaptation of software written for HP and Tektronix engineering computers to the PC environment. TBASIC's enhanced instruction set conforms to the new ANSI proposed standard for BASIC and runs on many popular operating systems including: PC-DOS, MS-DOS, CP/M, UNIX, and VMS. Some of the supported computers include: IBM PC/XT/AT, AT&T 6300, HP-150 & In- tergral, Tektronix 6130 & 4170, and DEC VAX and Rainbow.

TBASIC has a unique assortment of user-friendly features such as an on-line HELP facility, cross-reference facilities for listing variables and referenced line numbers, a command for renaming program variables that aids in documenting new and existing programs by making simple variable names more meaningful, MOVE and COPY commands that enhance program editing capability by assigning new line numbers intelligently, and syntax checking that is performed automatically as lines are entered to give instant feedback.

TBASIC features a full compliment of binary, scalar, array, matrix, and scientific math, including standard trig, transcendental, and other functions using both integer and double precision floating point data types. It also supports 8087 coprocessors. Special array functions perform area, circumference, centroid and other calculations for polygons. Other features include line labels, block IF statements, true subroutines and other features.

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problem if you haven’t assembled your own Forth compiler. While figuring out how it is done, I also considered writing or designing my own language as the structure and concept are pretty simple. Forth is most confusing only due to the use of the cryptic words. For those, like myself, who mostly do assembly work, we have already learned all the three letter codes that represent data movements in assembly. Forth words are no different than these, and the comparison can be used to help understand how it all works.

Suppose we want to get data to a terminal in assembly this is usually called CONOUT. The routine would first check to see if the device is ready, then get the data and output it to the device. Next it would flag to indicate completion, then return to the main program. The way Forth works is to break each individual operation into words (think of MACROS). Our assembly program would then be CONOUT: TEST GETDAT OUTDAT FLAG RETURN. When assembling the macro program, the assembler would actually write the code and assemble it if you coded it by hand. Forth, however, would assemble the addresses of the machine code portions that perform the functions and then CALL them in sequence. The difference in code sizes is due to the fact that Forth only assembles the machine code once and then references that location there after.

The macro assembler, however, would insert machine code whenever the routine was used. Suppose that CONOUT used 25 machine code instructions and our program called it 10 times. If one call instruction is considered as one instruction, then the Forth program would be 25 + 10 for a total of 35 machine instructions compared to the macro’s 250 instructions (10 times 25). The macro version might actually be faster without all the jumping, but the Forth version would be much smaller. Optimizing the Macro code would have you calling the routine where the code was placed instead of repeating the code every time it was used. The size of the optimized Macro code would be the same as the Forth code, but faster than Forth’s threading.

I have been considering creating a language based on Forth’s threaded features but using the microprocessor type architecture. We could take the Z80 assembly code and call this the language, with the kernel being actual

Z80 machine instructions. For use in other machines, simulation of the Z80 instructions in the kernel is all that is needed. Think about it, what it means is being able to disassemble a program or use a PROGRAM.ASM file, load the program into Z80 Forth and have it run directly on a 68000 machine! In reality I am borrowing Moore’s concept of a universal design except that mine would be register oriented instead of stack oriented. All other features would be the same.

Enough speculation, I’ll have more on the ROMing of Forth later and will see how many people can find errors with my idea of a new language.

Televideo TPC-I
A recent release of the discontinued Televideo portable Z80 computers, called TPC-I, at $795 has caught my attention. I purchased one for my mother-in-law to use at college and decided to check it out. The case is all plastic, so getting into it was a big chore. It all snaps together even though there are screws (mainly for looks). A board that covers the whole end of the unit has the computer. It has two Tecs FD55B drivers, a nice 9 inch monitor and 64K of memory expandable to 128K. The disk controller is a 1770 (5 inch only), and there is an R5845 video controller for the GEM package. There is a Z80, with several special chips handling unknown functions (no schematic provided). My unit is clean with only one jumper on the board (rev. B1) and it physically looks good except for the case. Other than adding more memory I would guess the unit could not be changed or modified (no adding 8 inch drives!).

If you are not concerned about the hardware, the units seem to work pretty well and come with a full complement of software. I am not necessarily recommending that you buy one of these, but only letting you know that they work, appear to be made OK, but will not let you expand on them, which is what I always need to do.

Portapac
What I can recommend for those who carry data from one place to another is the Portapac by Cryptonics (11711 Coley River Circle, Suite 7, Fountain Valley, CA 92708, phone 714-540-1174). This rather small $500 unit can intercept serial data and store it in memory for playing back later or elsewhere. We bought this unit to help with an industrial problem. mainly slow and troublesome tape cassettes. We needed some way of taking data from one location, where the controller was, and bring it to our shop where a regular computer could debug it and make changes.

Now that we have the unit in our hands, all sorts of uses come to mind. It can fit in between a computer and a terminal to trap data for later use (could prove useful with mainframes that will not talk to other computers). The units also can help with changes in baud and protocol operations by allowing you to store the data (the unit has battery backup), change formats, and play the data back. One use I can think of is in a busy office where the printer is located in a quiet area and the computers are all over the office. You could get one of these for everybody and just have them take it to the printer for use, no messy or long cables going to all systems. Also let’s not forget the protocol problems (most LAN’s will cost more per unit than this). The Portapac can handle up to 256K of memory (ours came with 32K and can be expanded to 64K by adding 6284L-P15’s). It looks rather simple inside and well built, so give Cryptonics a call if this fits any of your needs. I may build one for myself along similar lines and if so will write it up in The Computer Journal.

(Continued from page 50)}
I made it back alive from the SOG (Semi-Official Get-together sponsored by Micro Cornucopia) in Bend, Oregon. This beautiful recreation area of central Oregon was the site for a get-together that brought in several people from outside the U.S. At the barbeque on Thursday night, a showing of hands made it clear that the largest part of the attendees were from east of the Rocky Mountains (rather impressive for a free event).

Things got started Friday with seminars and product displays. About 300 people were there Friday with more people expected on Saturday and Sunday (those poor working types). While we were only able to attend the Friday stuff, there is no question that all three days were a success. Some of the speakers had a lot of information to give (even though the titles of seminars were a bit cryptic). Only a few of the seminars had conflicting times so you could stop in anyway to see if one fit your needs. Jack Denton, the author of RPM (which I reviewed some time ago), started the session with an overview of CP/M which really turned out to be a discussion of the known bugs in CP/M and how to correct them. For us system type programmers, this was great. Later on there were talks on OS9, a panel on 32 bit systems, and on, and on. A sample of what you missed if you weren't there was the new Z80 replacement (Hitachi's HD64180).

Z80 Plus

It was most unfortunate that the speaker at the SOG had only been in the United States for THREE days and his English therefore, was rather poor. This talk has since been supplemented with documentation, which makes this new chip by Hitachi, the HD64180, a real plus to the 8 bit world. The unit is capable of addressing 512K of memory, has two serial ports, a timer, DMA functions and high speed clocked I/O for chip to chip communications. All this is placed on top of the Z80 architecture and in CMOS for higher speeds (10MHz possible, 8MHz nominal, 8MHz now!) and less power consumption.

The speaker was representing the MSC BOARDS imported by Southern Pacific Limited (Box 4427, Berkeley, CA 94704-0427). This company has produced a board having 512K of memory, serial and parallel ports, disk controller, video controller, and HD64180, all on a Five inch disk drive size board (same as AMPRO). As if the size and features were not enough, the price was even better — $400, and all available in a month or so. If this product doesn't get Zilog to produce their long awaited Z800, nothing will.

The HD64180 has a few new instructions, including an 8-bit multiply, a sleep command, and some special I/O block moves. Add this to their extensions of the bit test functions, and you have a faster Z80 machine. The MMU (memory management unit) part of the HD64180 is pretty standard and will make it possible to program any 4K segment (of the 512K space) into the 64K operating system space. This is done by loading registers with offset values, with the necessary register selection based on the logical upper four bits (12 thru 15). The lower 12 bits are added with the MMU registers (7 bits) and the upper logical 4 bits to create the physical address. The preliminary data sheets didn't have the actual code needed to do this, so when I get the big manual, I will cover more of the features in greater detail then.

The Main drawback of the unit is the 64 pin socket. There will most likely be a shortage of these at the start, so using the chip will require headers at first. A project that I will undertake when more information is available (and the units are for sale here in the U.S. at a reasonable price) will be an adaptor for Z80 machines to make use of the new features (such as extended memory addressing). I am thinking of some kind of paddle board that will have a Z80 header, a socket for the HD64180, jumpers for extended memory (or sockets for more memory!), and serial buffers to make use of the extra serial ports. This shouldn't require more than five or six components (not including memory). The biggest problem will be getting the HD64180 and enough information to make it work.

Forth ROM

An ongoing project of mine is putting Forth in ROM, and I have some information on doing that. It didn't take me long to understand Forth (although learning all the words may take forever!), but seeing how it works in real code took some investigation. Now I haven't figured everything out yet, but the way it is put together is quite nice. It reminds me of an article I published in my local club's newsletter about writing your own language. The idea was that a good macro assembler could be used to create enough macros to do all your functions. Writing a program would involve calling only macros and no actual code. Forth is similar to that idea with some other features, and of course the threading is different.

I will leave threading for a later date but point out some important thoughts now. From what I have read about Moore (the author of Forth), it would appear that he based the design of the language on an HP calculator. What he did was to create a processor architecture in software. The original work was done on big mainframes and all his early calculations were probably being done by hand on an RPN calculator (Reverse Polish Notation machine). My biggest problem with Forth is the RPN logic, which is a carry-over from the early calculators. The idea, however, of making a processor in software is pretty terrific as it can be transported to any physical system. The actual codes to create the basics of the design (stacks and math functions) are done in the main kernel, with more advanced functions calling these routines.

The ROM code of course doesn't have any buffer spaces inside the ROM. Other than that, it would be the same as regular Forth code. While this concept is pretty well known, the actual putting of code into ROM can still be a